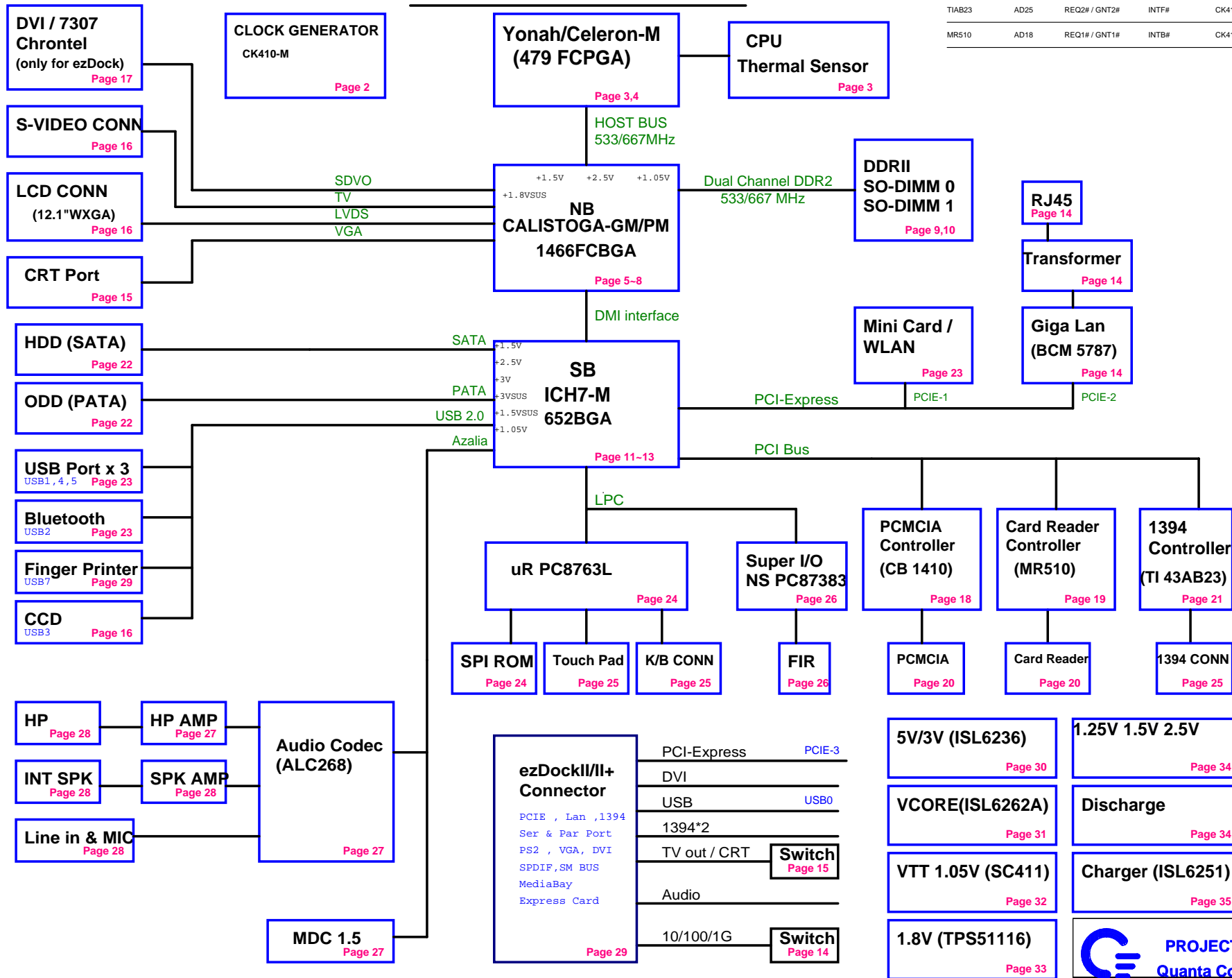
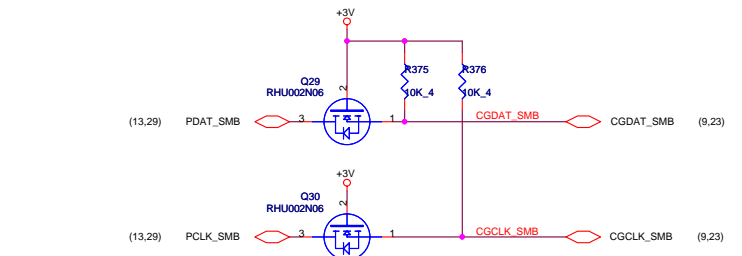
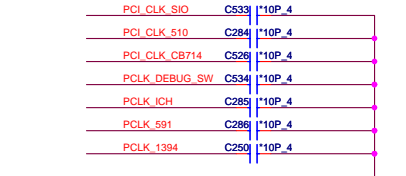
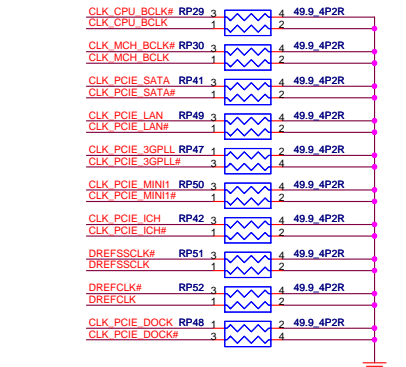
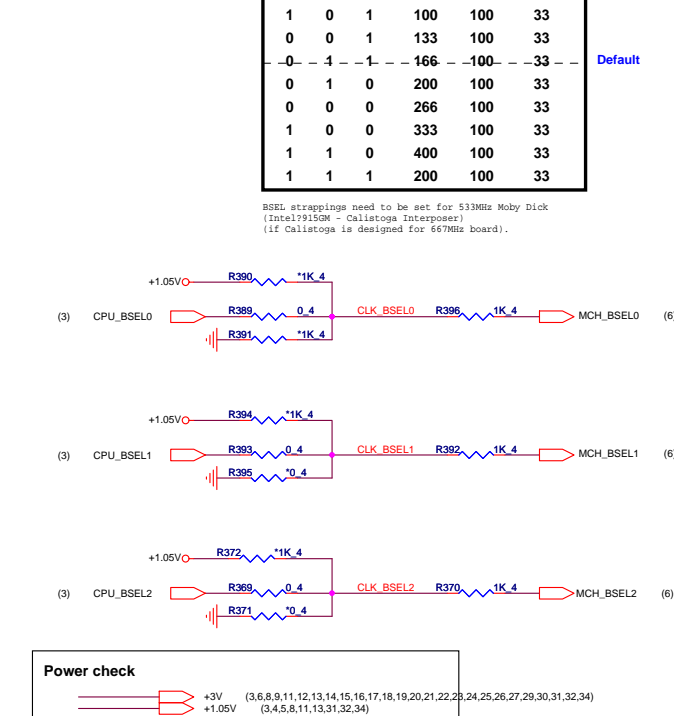
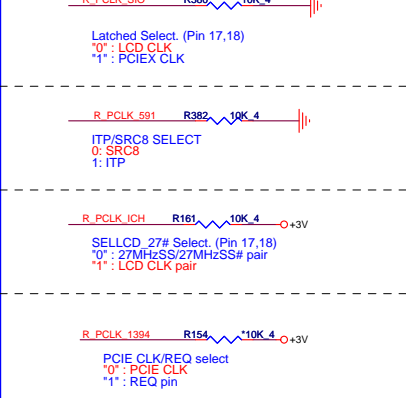
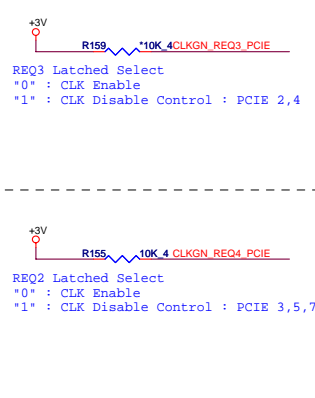
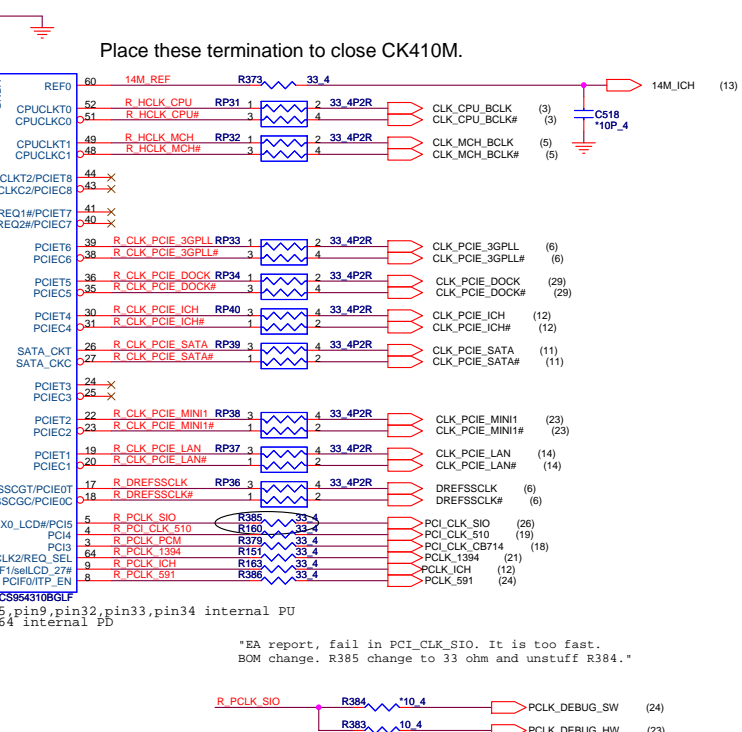
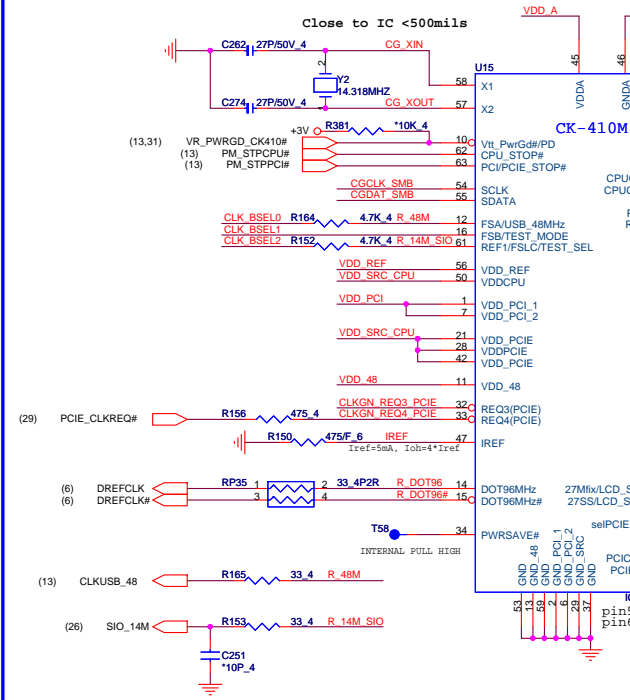
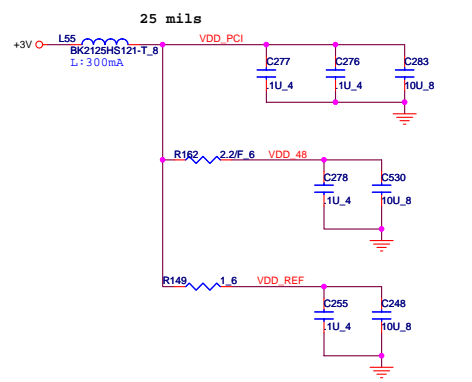


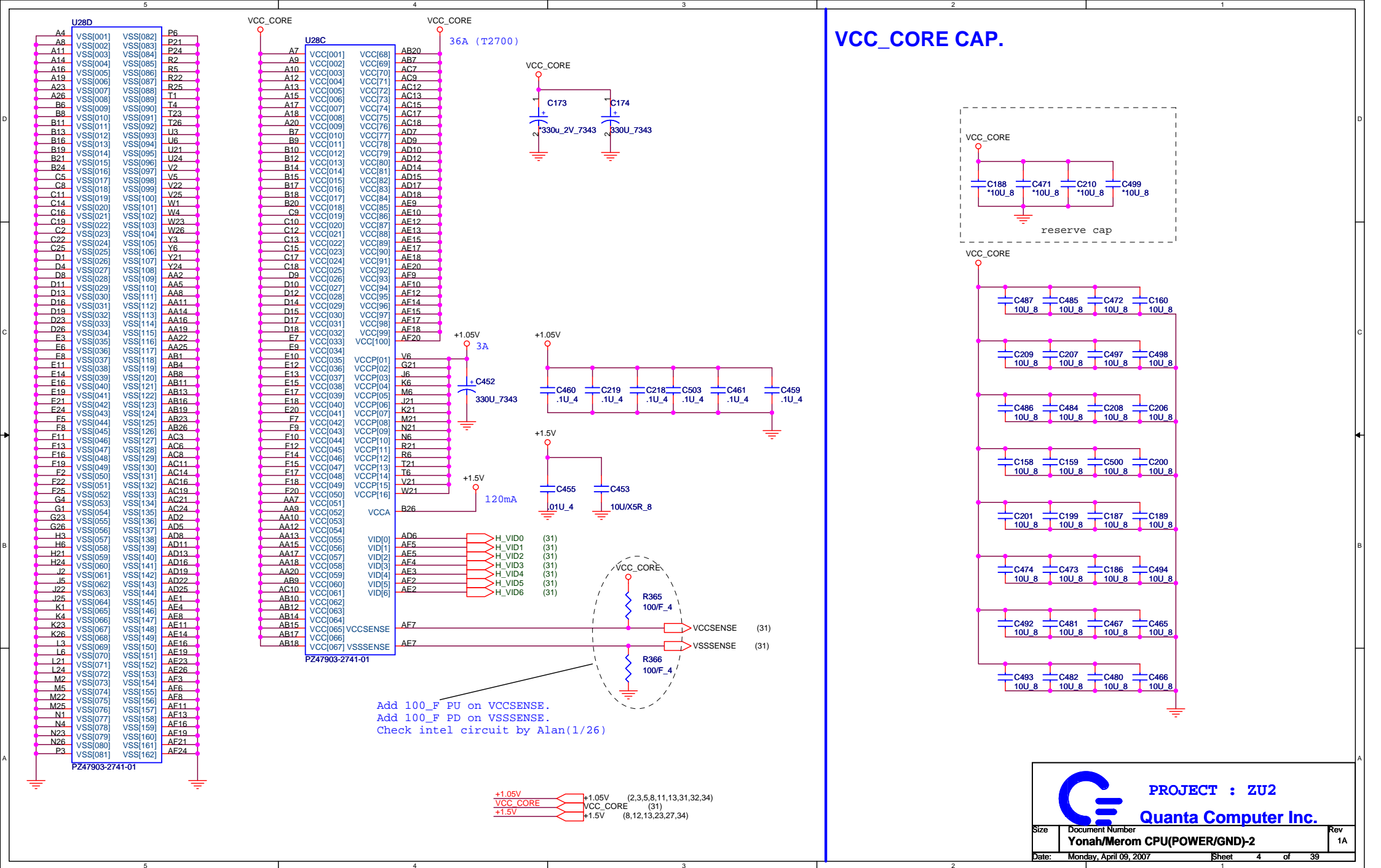
# ZU2 SYSTEM BLOCK DIAGRAM

PCI DEVICE	IDSEL#	REQ# / GNT#	Interrupts	CLOCK
CB1410	AD17	REQ0# / GNT0#	INTE#	CK410/PC13
TIAB23	AD25	REQ2# / GNT2#	INTF#	CK410/PC18
MR510	AD18	REQ1# / GNT1#	INTB#	CK410/PC14

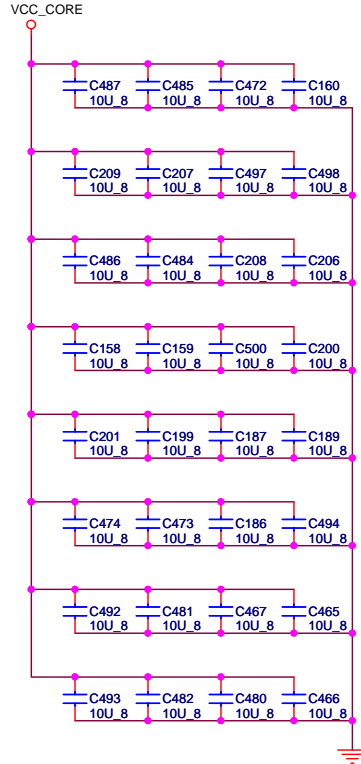
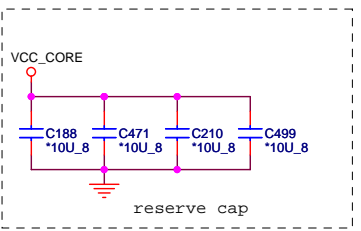









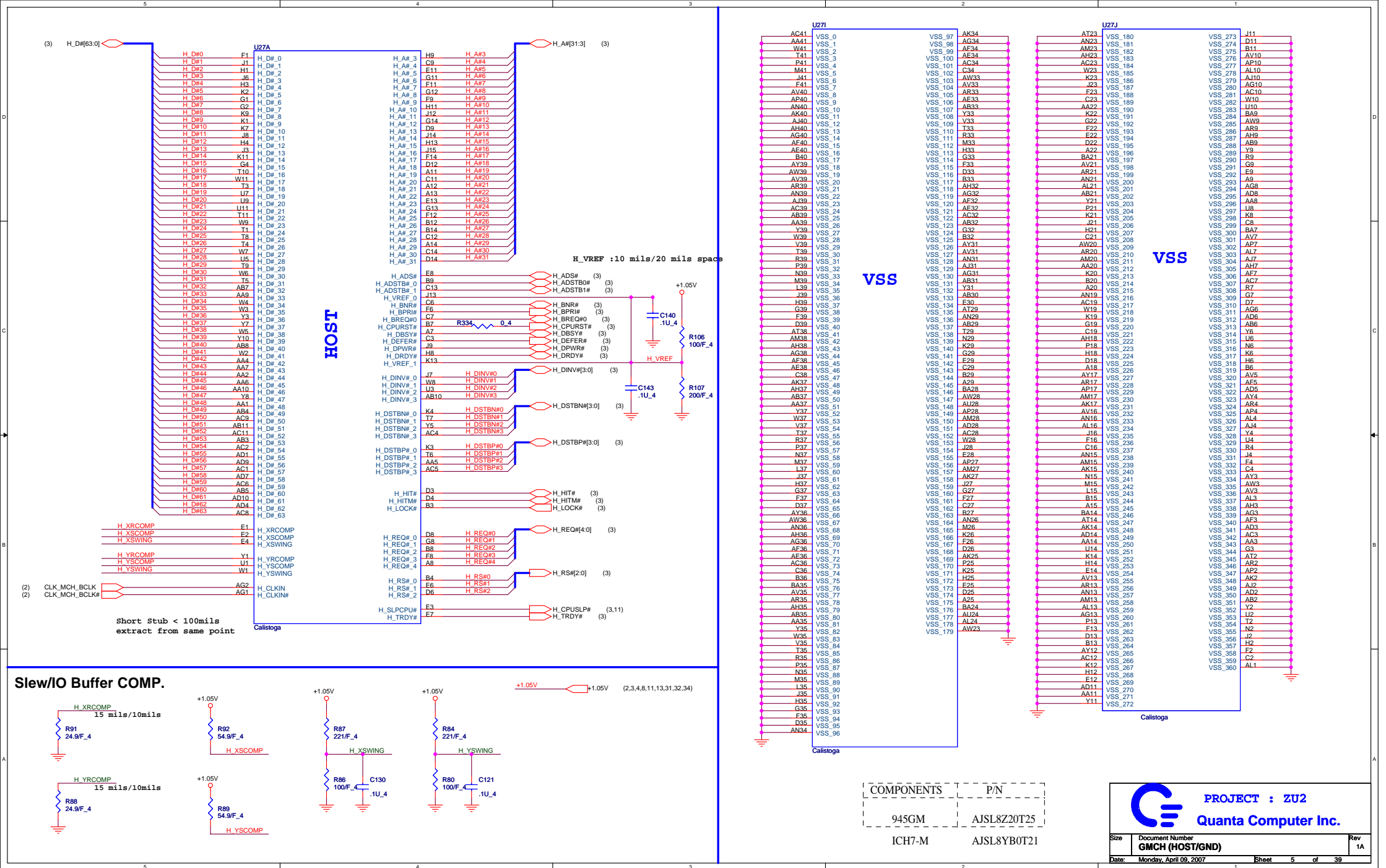
# VCC\_CORE CAP.



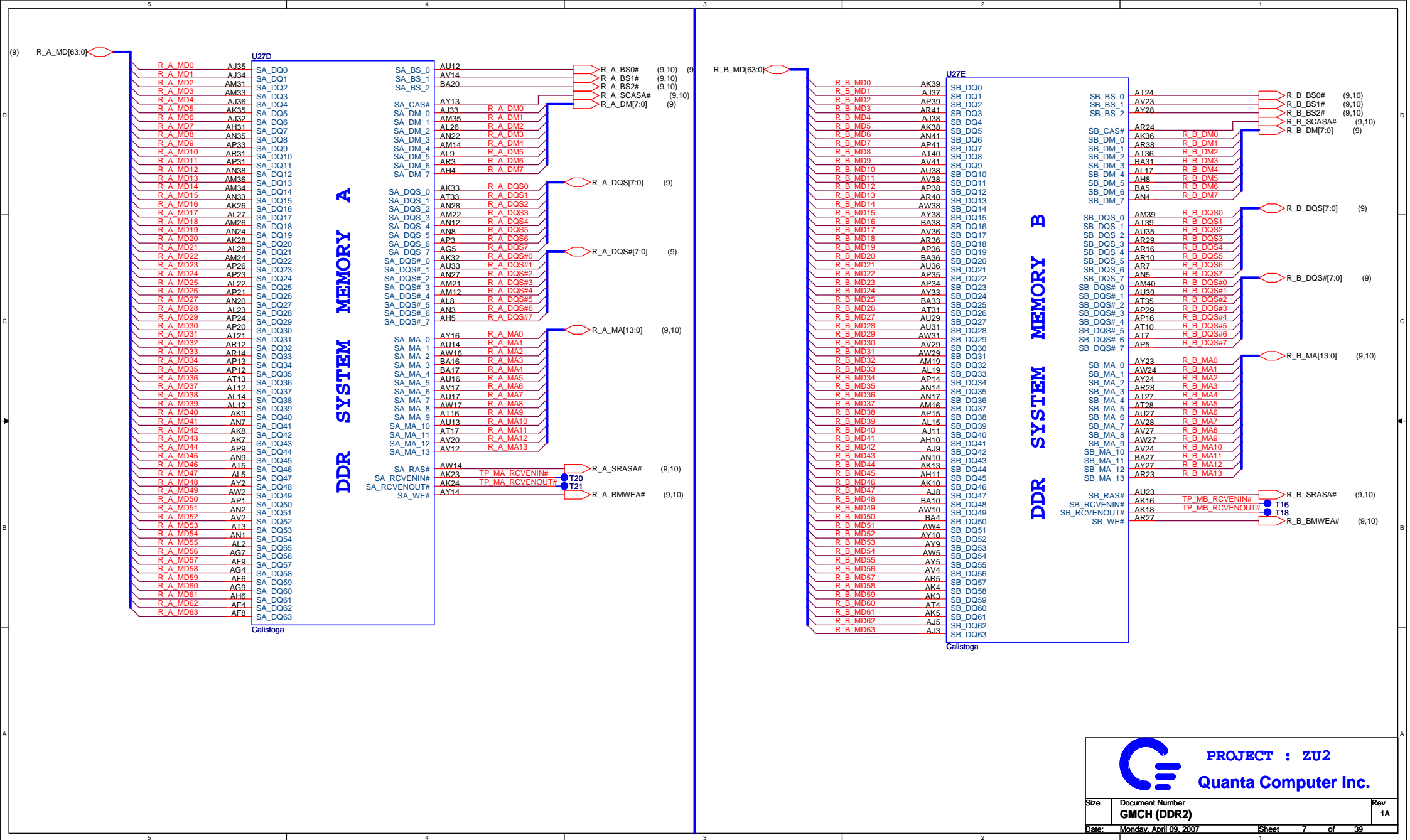


PROJECT : ZU2  
Quanta Computer Inc.

Size	Document Number	Rev
	Yonah/Merom CPU(Power/GND)-2	1A
Date:	Monday, April 09, 2007	Sheet 4 of 39



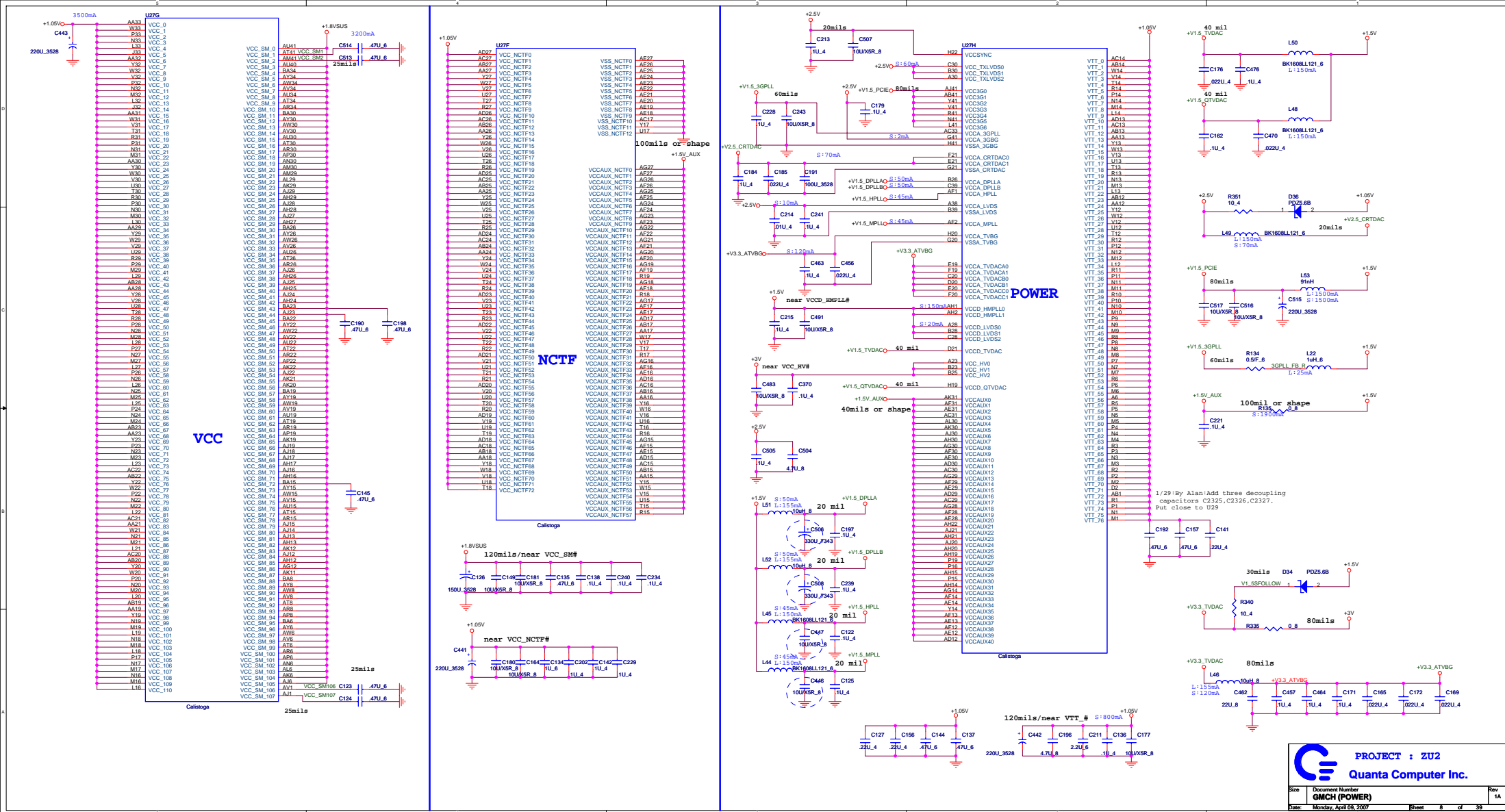


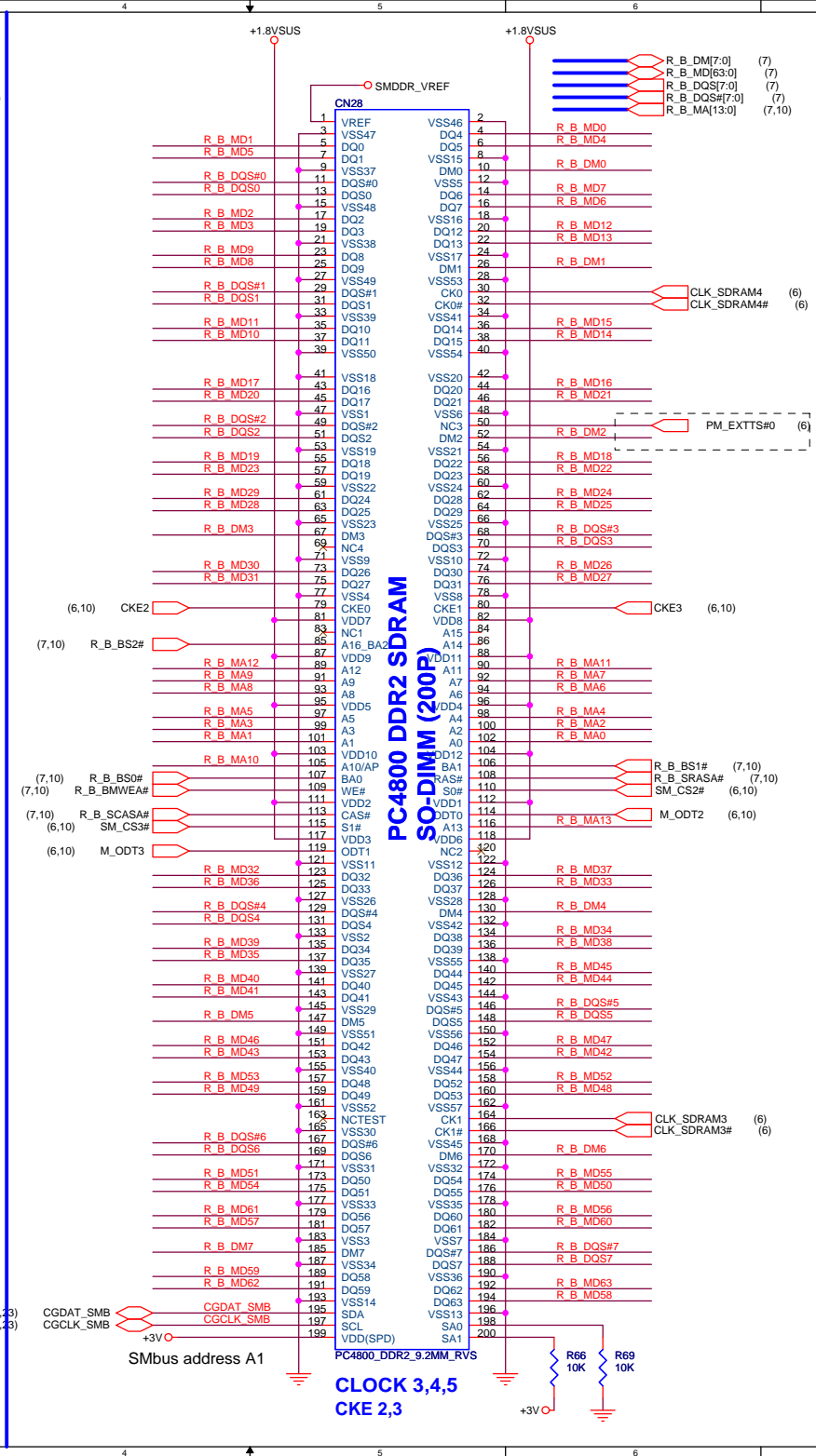
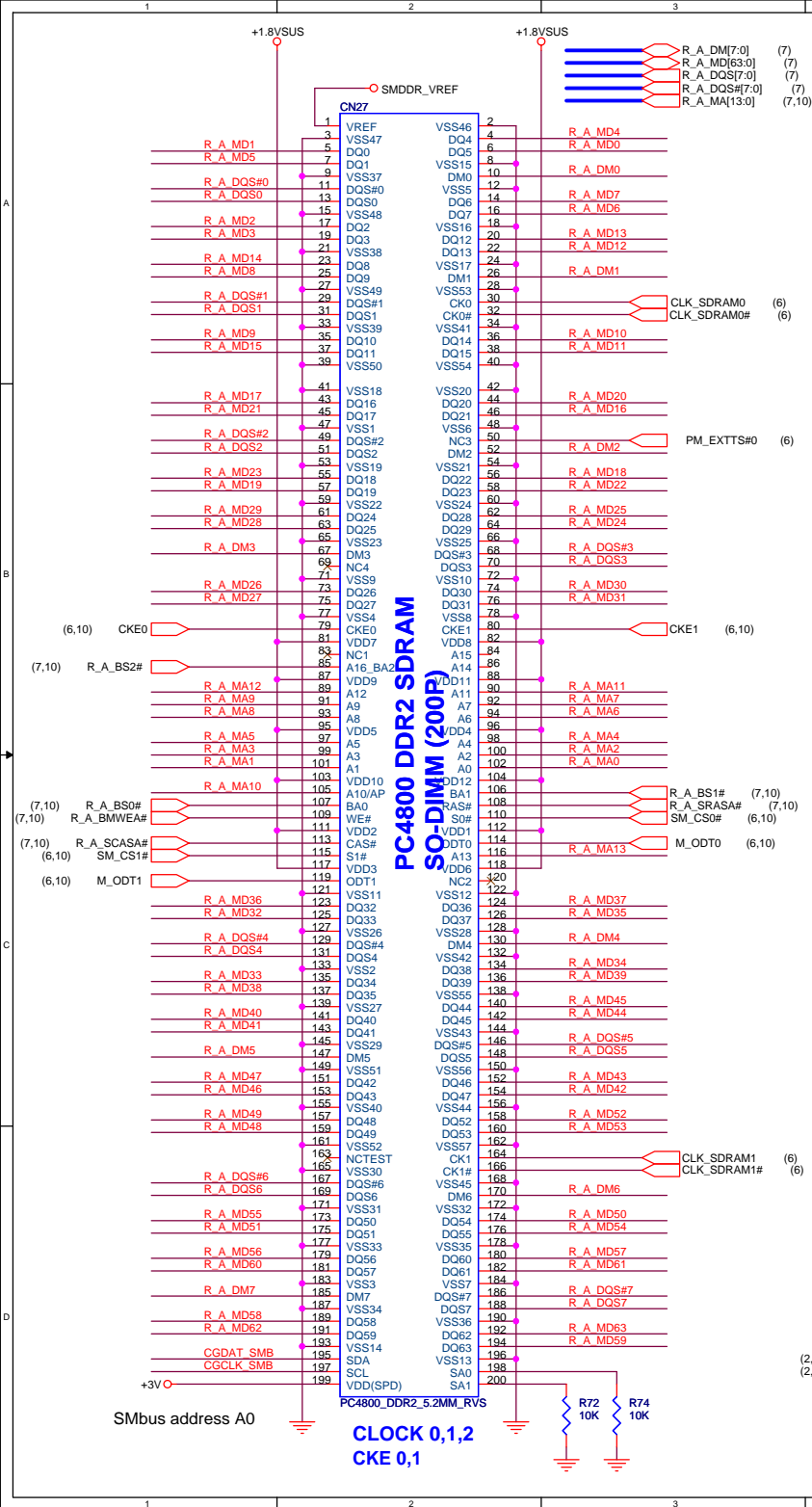


PROJECT : ZU2  
Quanta Computer Inc.

Size	Document Number	Rev
	GMCH (DDR2)	1A

Date: Monday, April 09, 2007 Sheet 7 of 39





**PROJECT : ZU2**

**Quanta Computer Inc.**

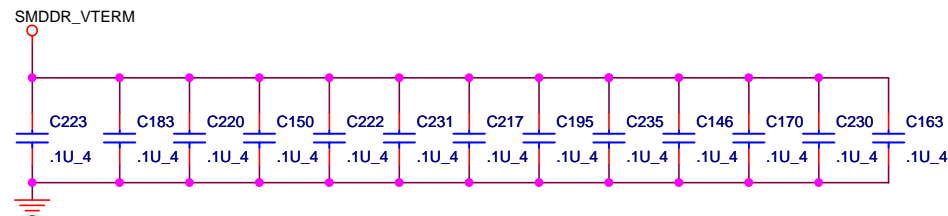
Size Document Number Rev 1A

Monday, April 09, 2007

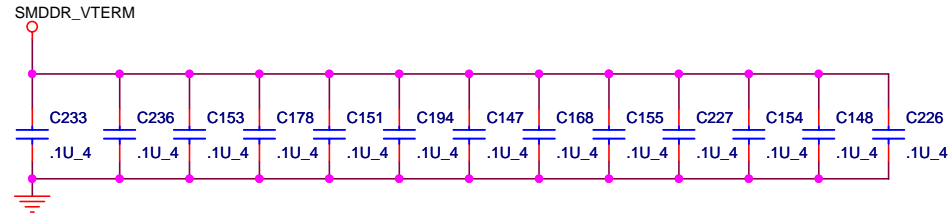
Sheet 9 of 39

Place Close to JDIMM1

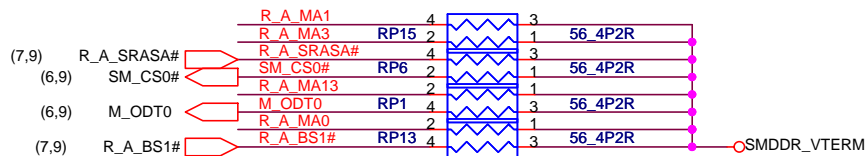
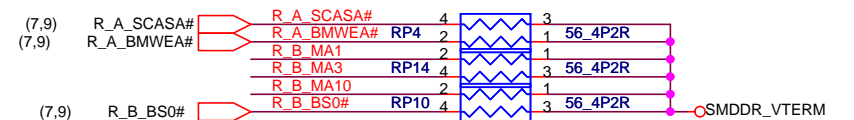
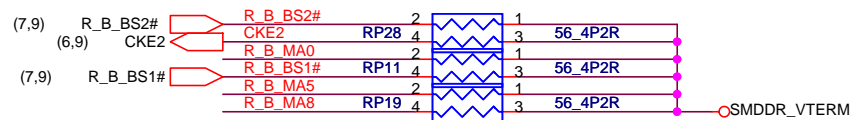
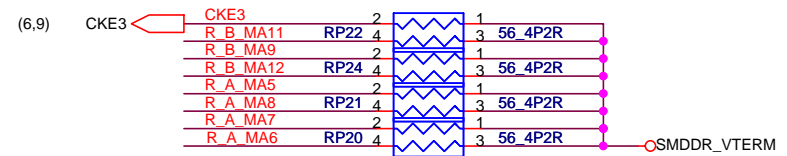
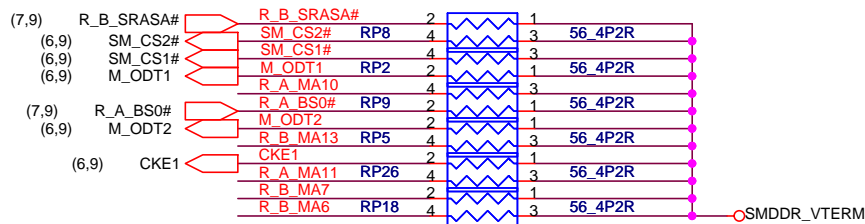
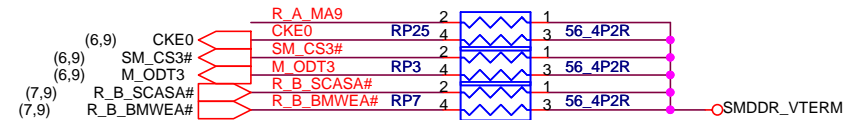
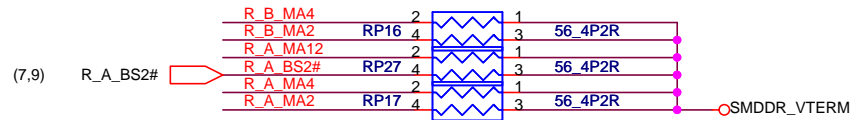
Place Close to JDIMM2



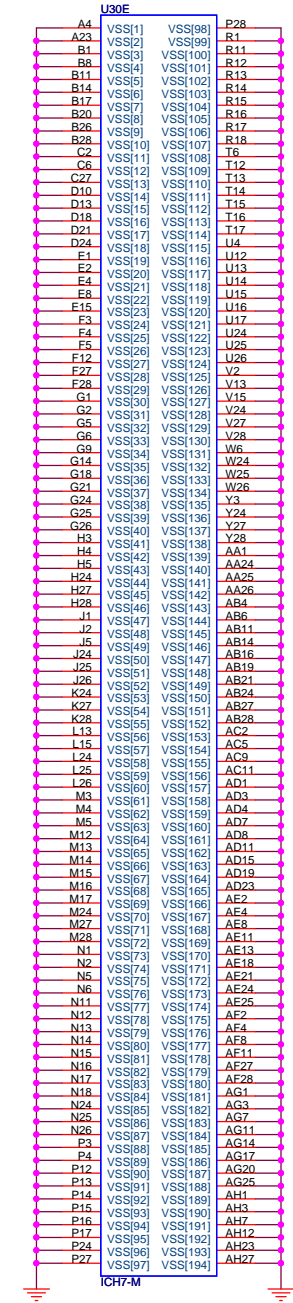
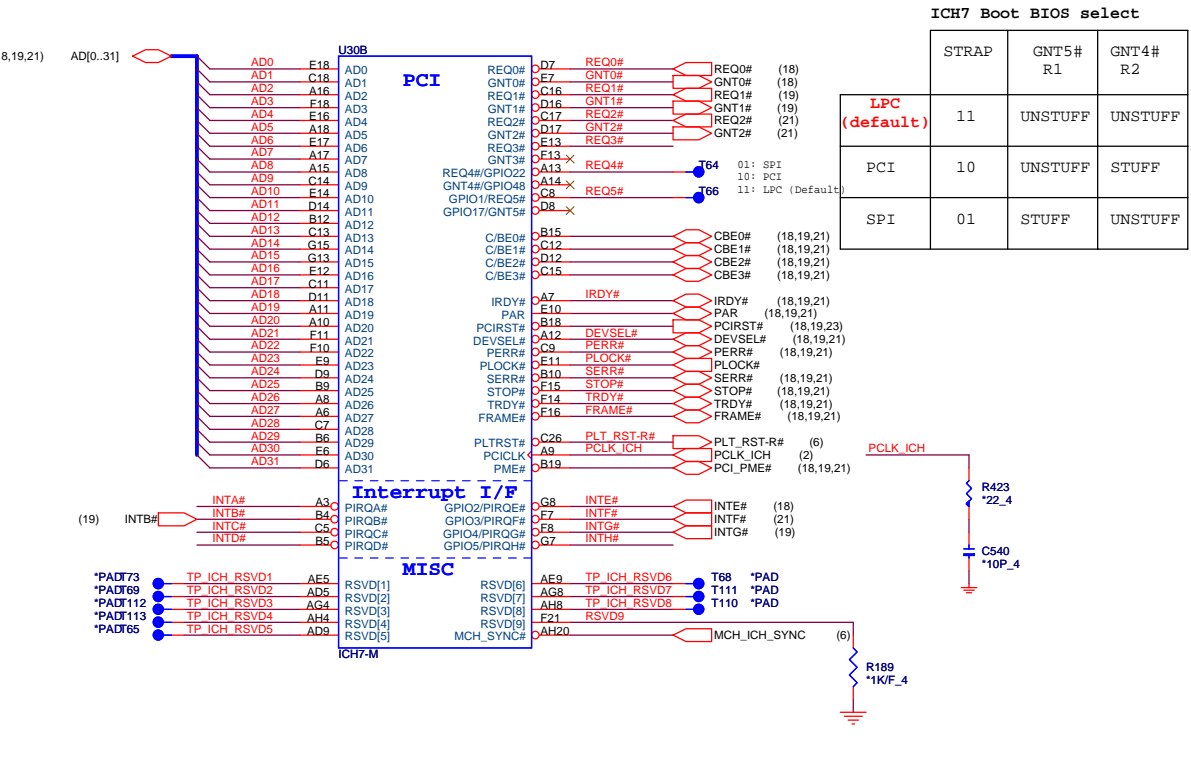
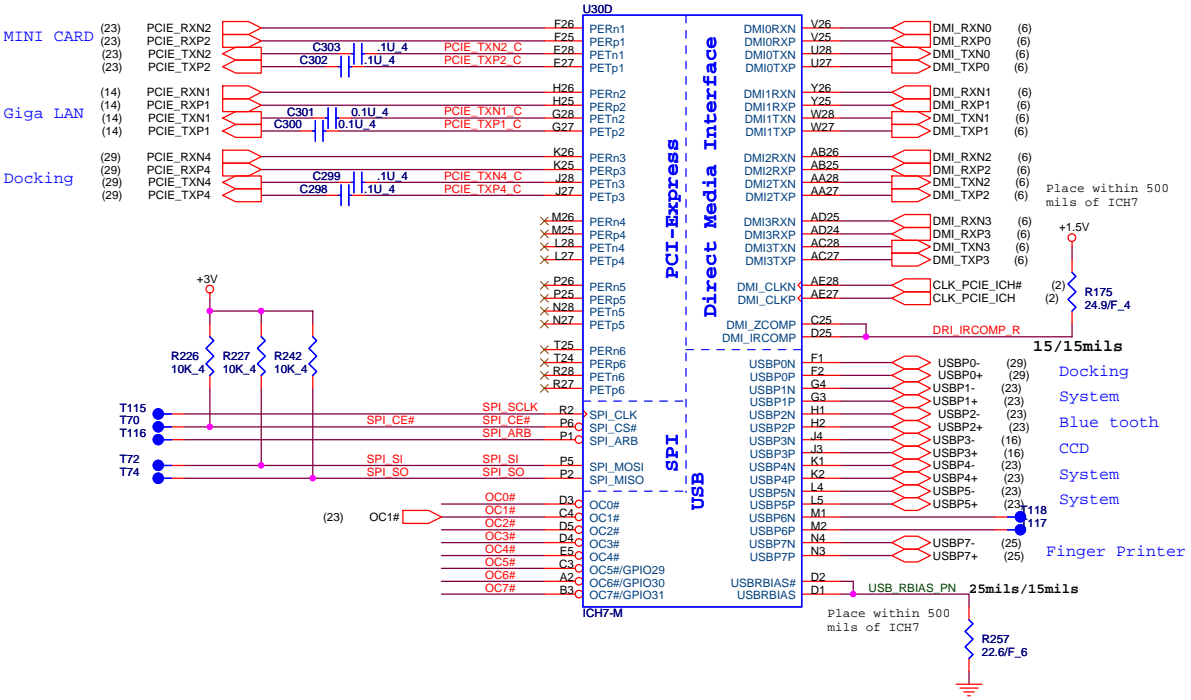
Layout note: Place one cap close to every 2 pullup resistors terminated to SMDRR\_VTERM



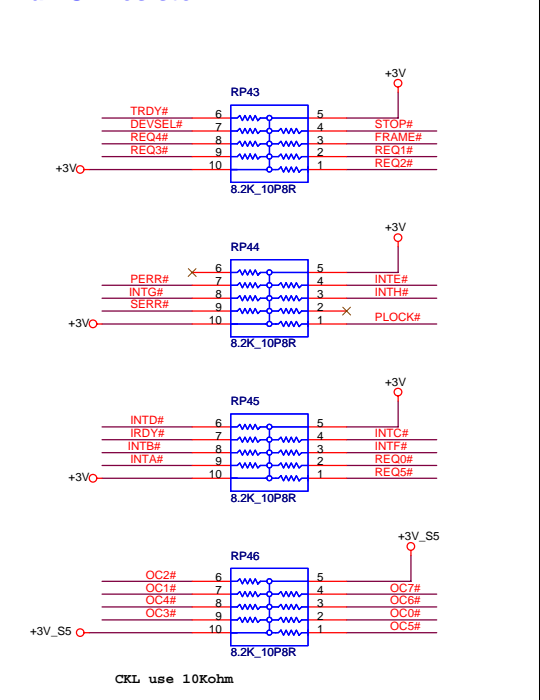
Layout note: Place one cap close to every 2 pullup resistors terminated to SMDRR\_VTERM



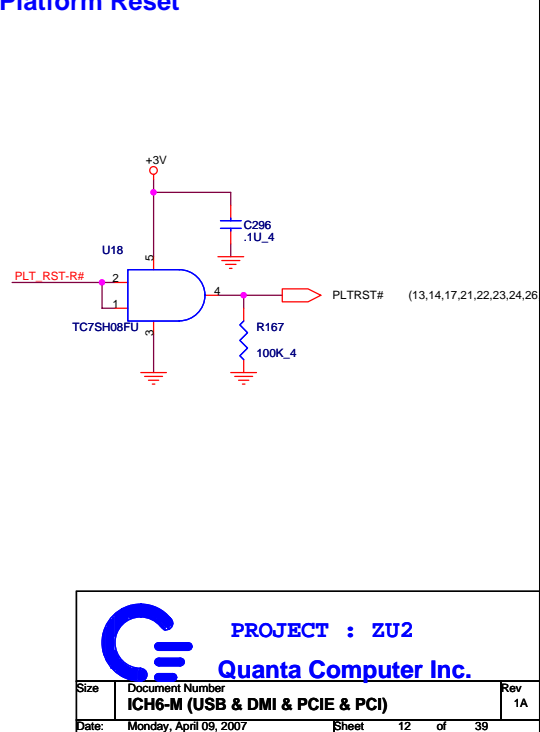


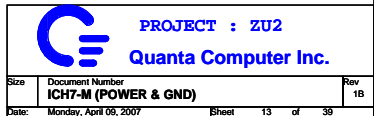
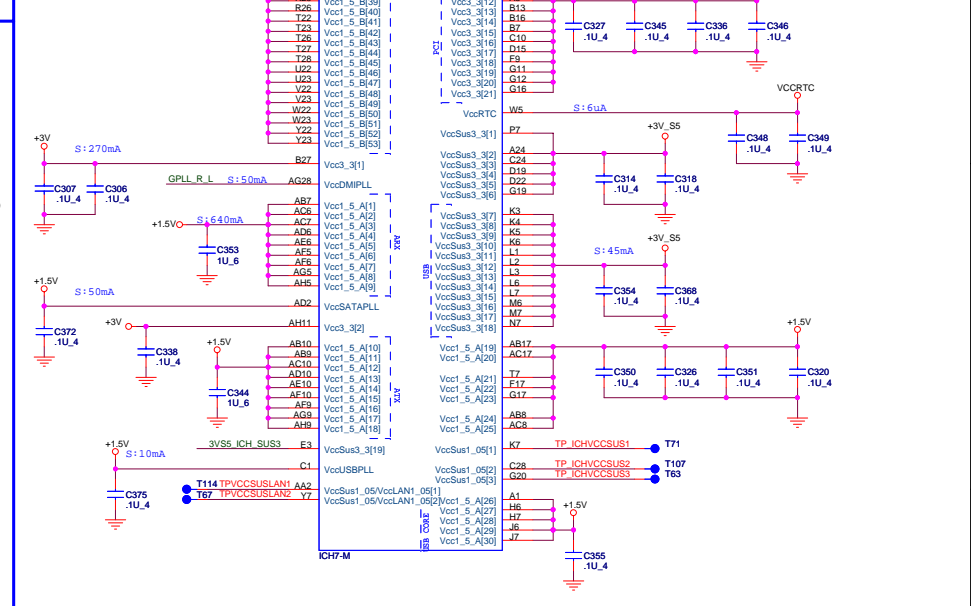
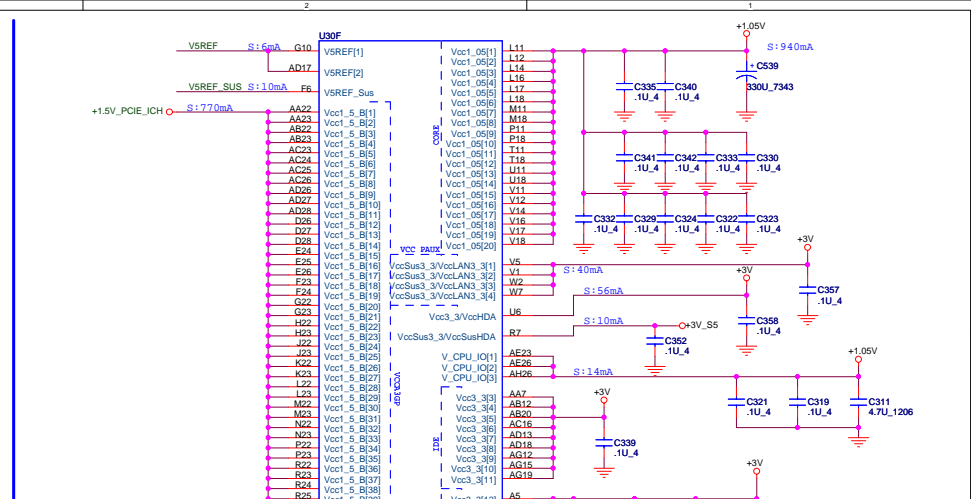


## Pull-UP resistor

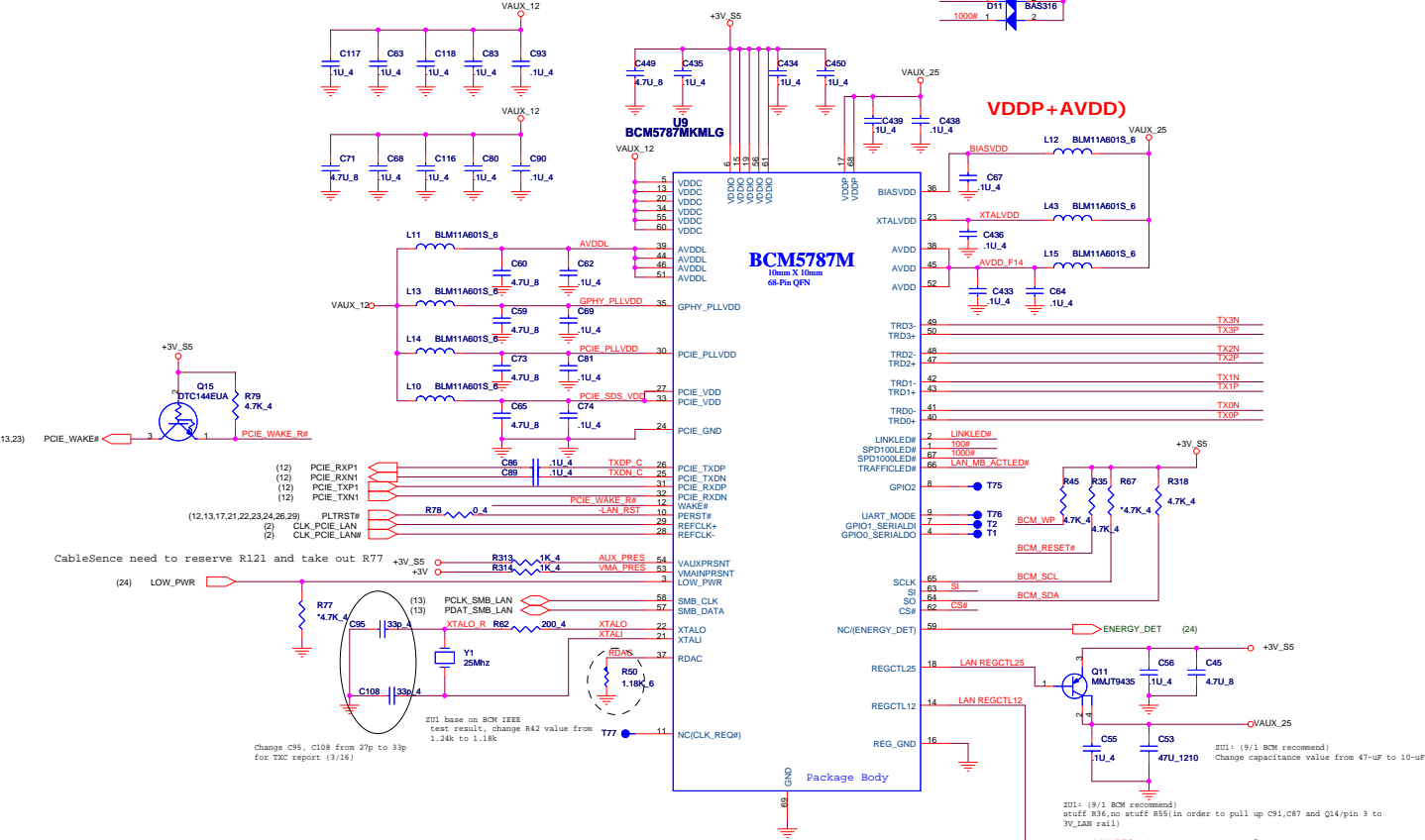


## Platform Reset

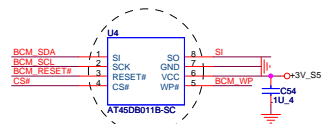




**Giga LAN BCN5787M**

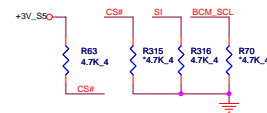


## Flash ROM/EEPROM

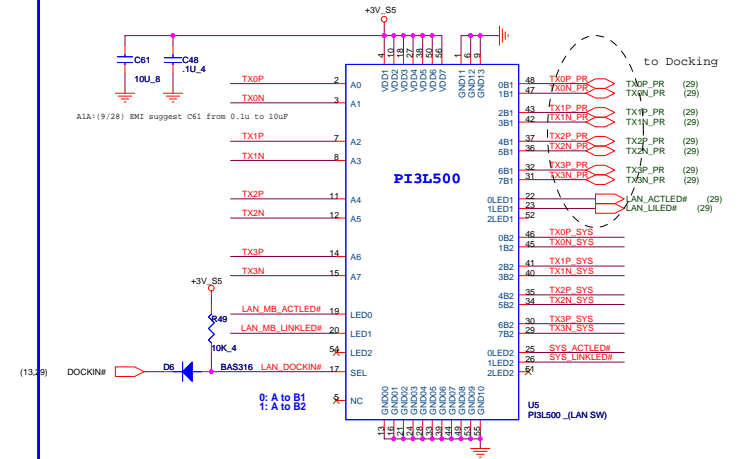


EEPROM Strapping

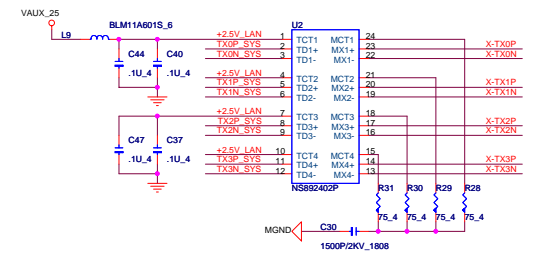
	SO	SI	CS#	SCLK
24c64	1	1	0	1
AT45DB011B	1	0	1	1



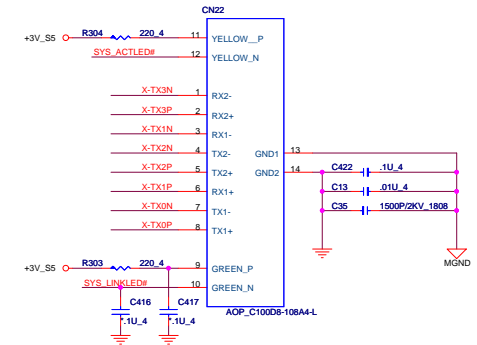
## LAN Switch



## LAN Transformer



## RJ45

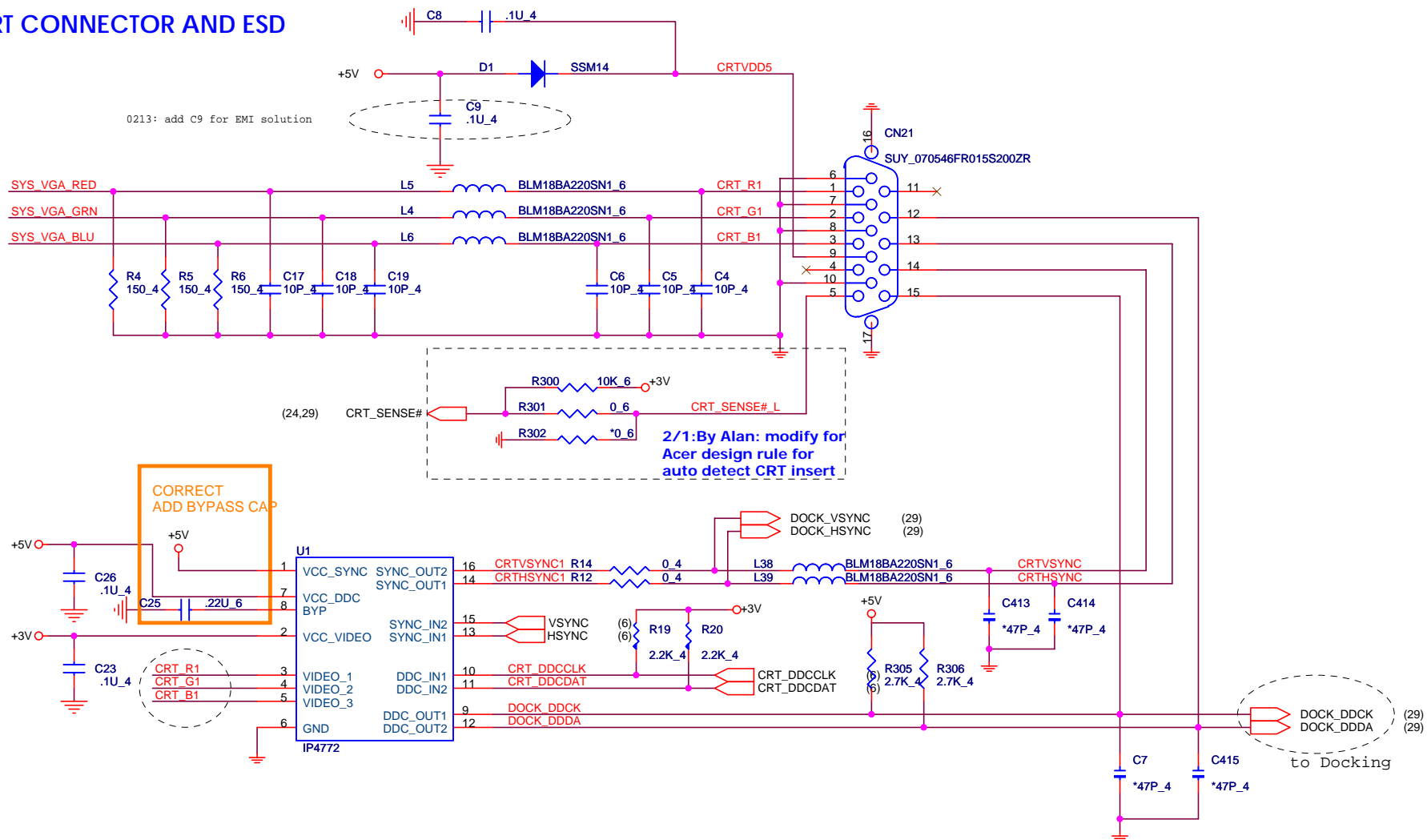


EMI request: reserve .1U (C416 and C417)for EMI Solution

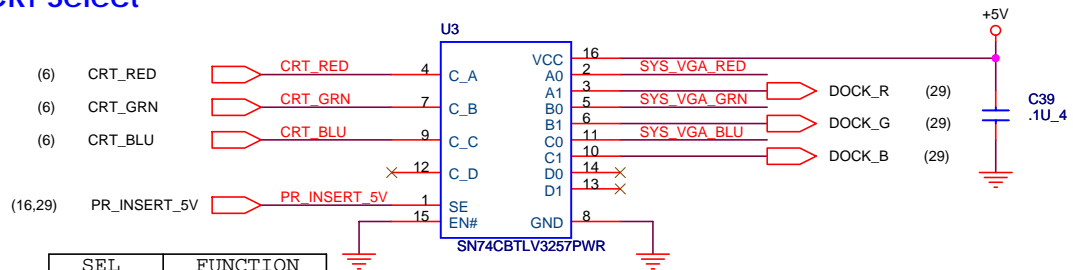


**PROJECT : ZU2**  
**Quanta Computer Inc.**

## CRT CONNECTOR AND ESD



## CRT Select



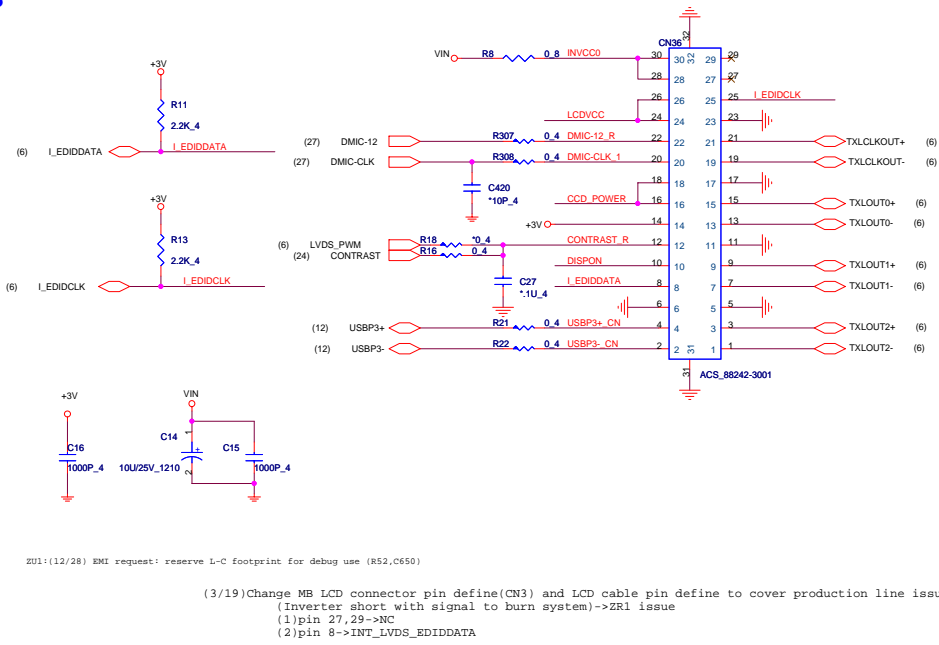
SEL	FUNCTION
LOW	IN_0
HIGH	IN_1



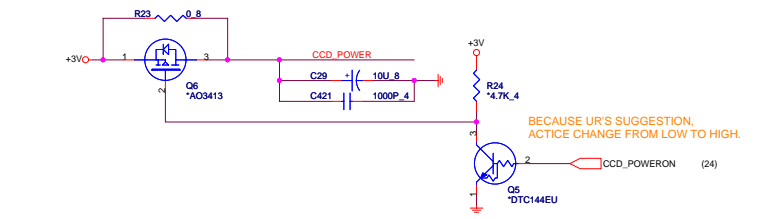
**PROJECT : ZU2**  
**Quanta Computer Inc.**

Size	Document Number <b>CRT</b>	Rev 1A
Date:	Monday, April 09, 2007	Sheet 15 of 39

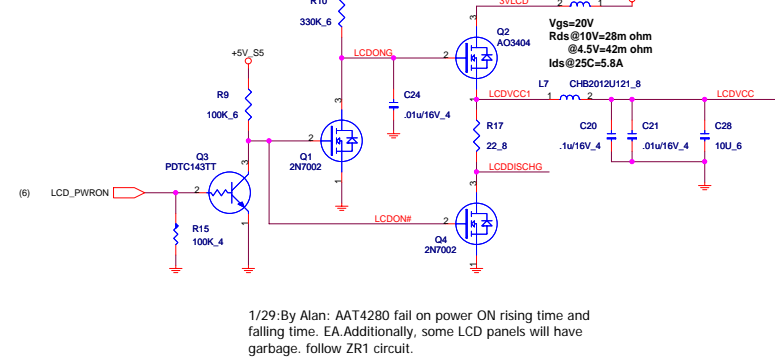
LVDS



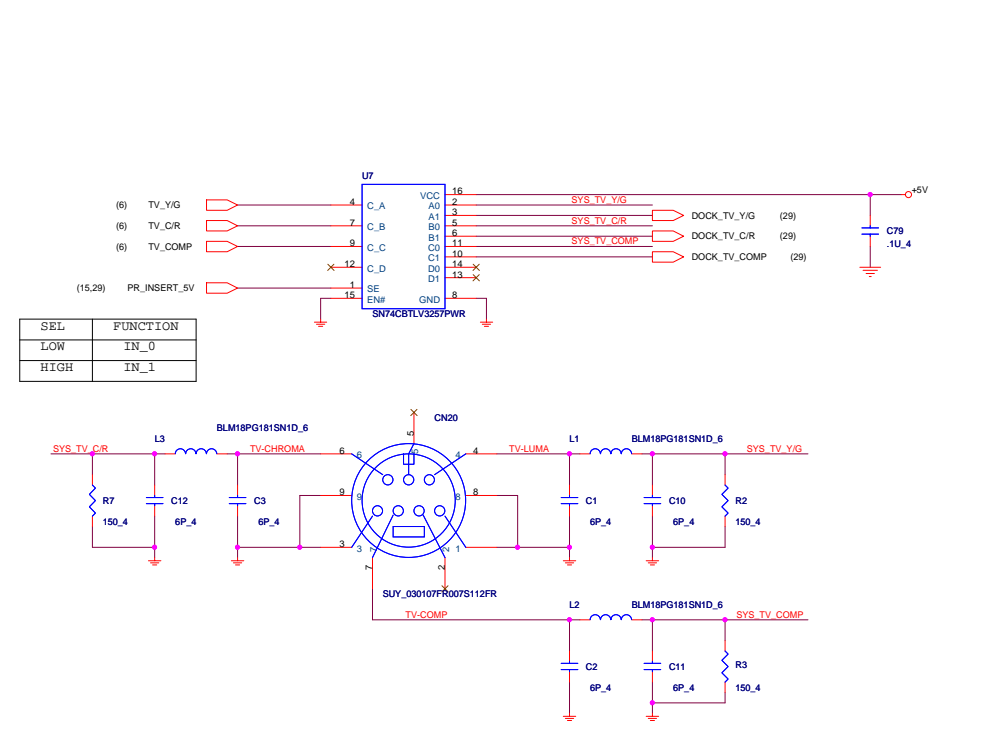
CAMERA MODULE



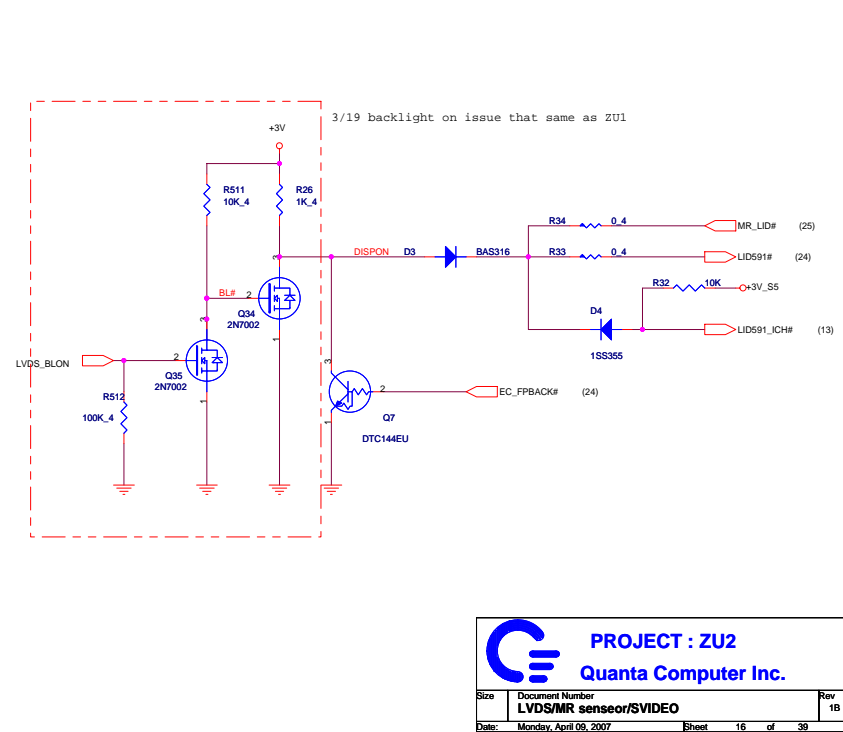
LCD Power



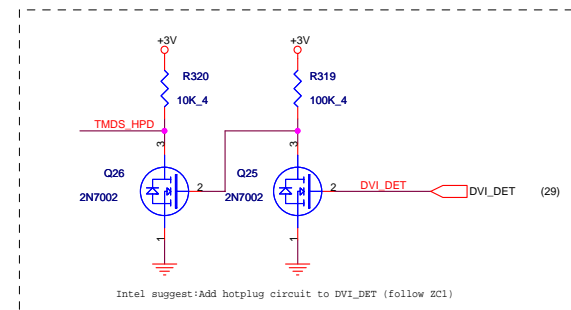
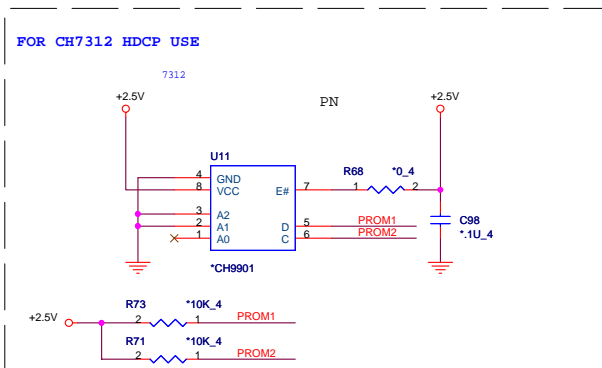
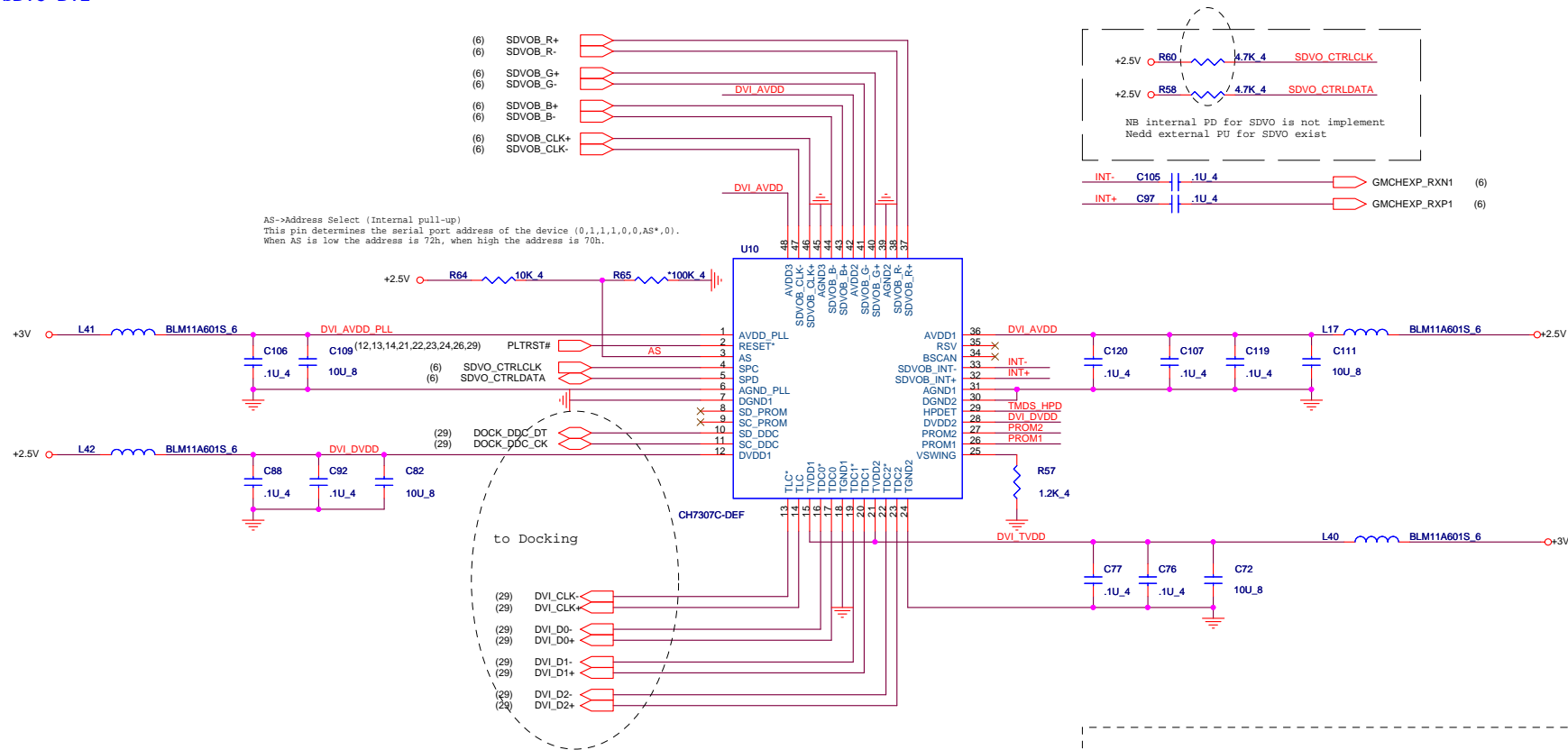
TV Out (SVHS) MiniDIN 7-pin

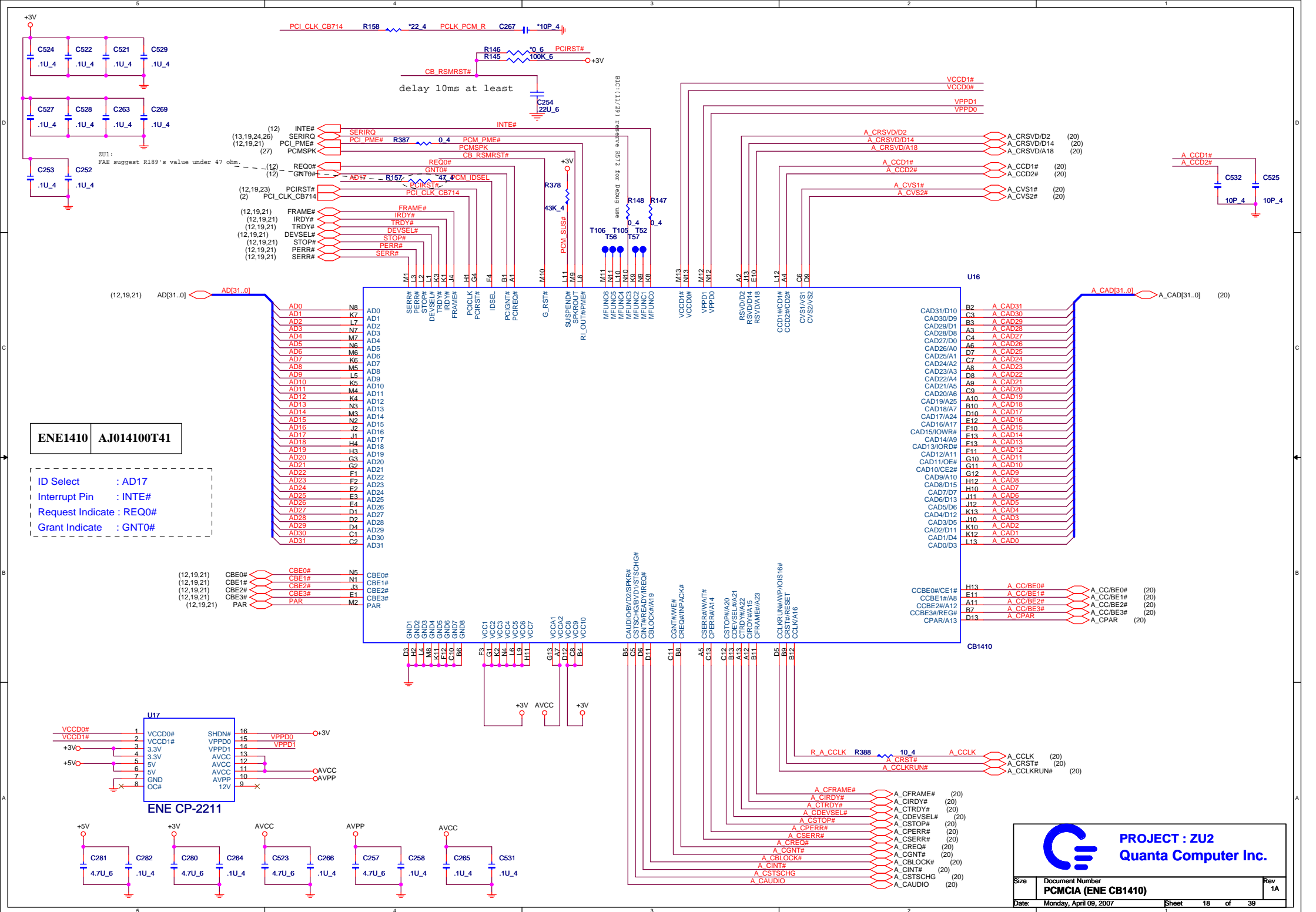


MR Sensor



## SDVO-DVI

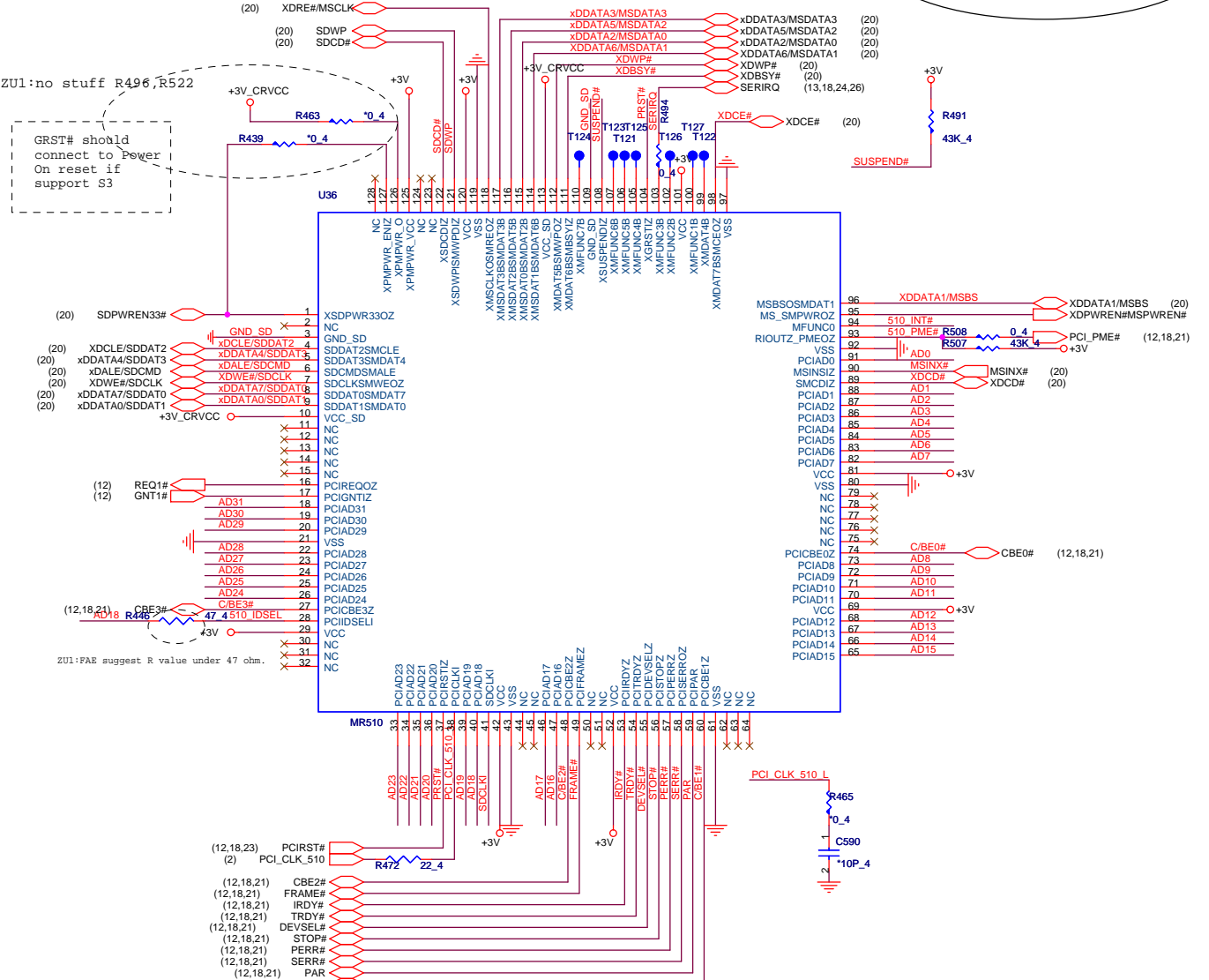




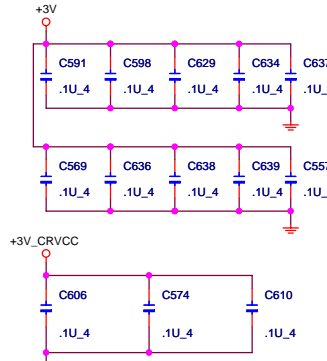
**PROJECT : ZU2**  
**Quanta Computer Inc.**

Size	Document Number <b>PCMCIA (ENE CB1410)</b>	Rev <b>1A</b>
Date:	Monday, April 09, 2007	Sheet 18 of 39

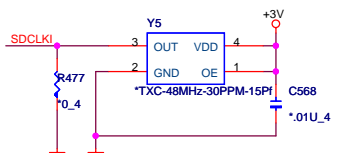
```
ID Select      : AD18
Interrupt Pin   : INTG#
Request Indicate : REQ1#
Grant Indicate  : GNT1#
```



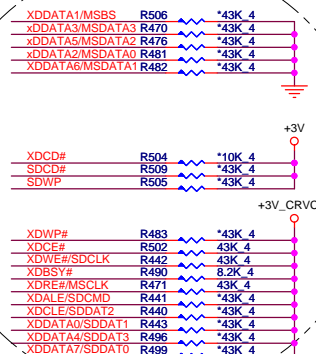
## Decoupling CAP.



## 48MHz CLK



PU/PD

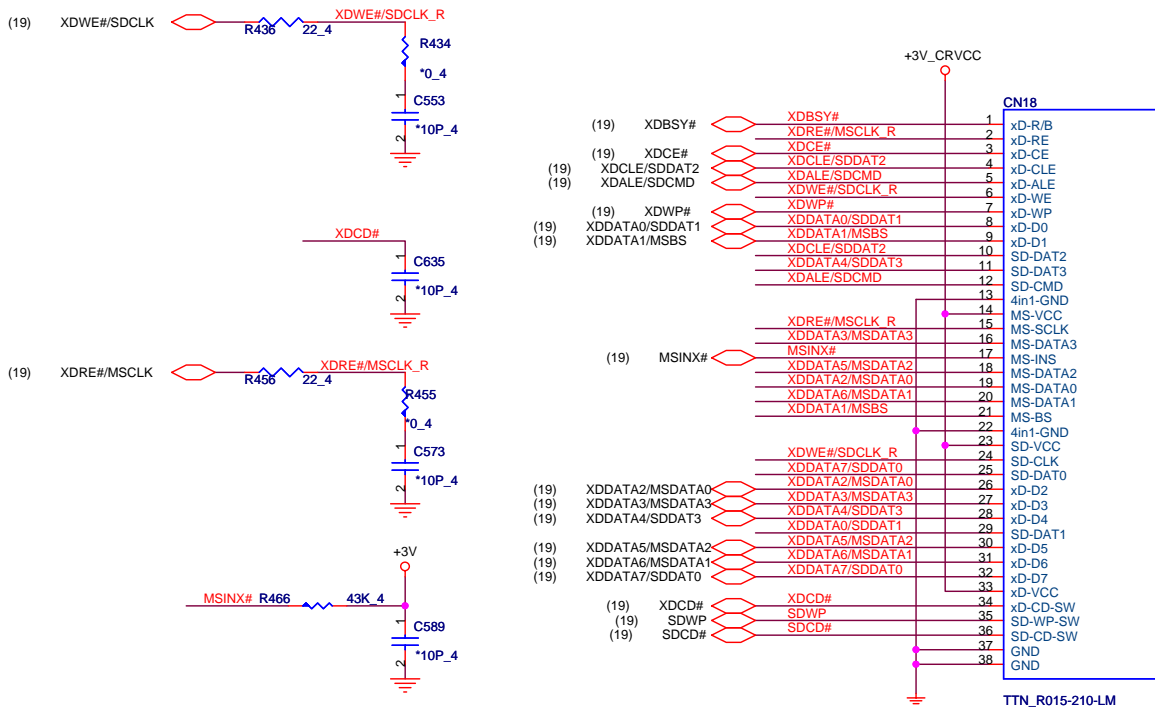


0213: follow ZU1 for cost down issue with FAE

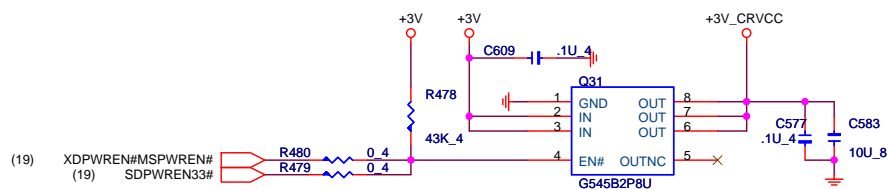


**PROJECT : ZU2**  
**Quanta Computer Inc.**

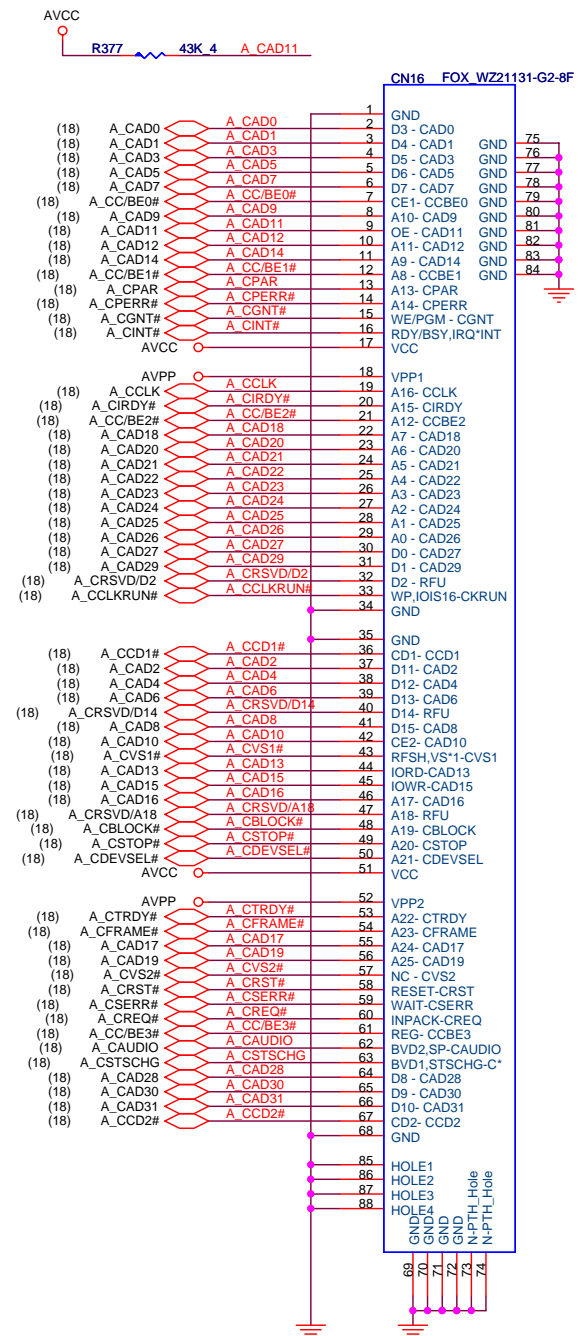
## CardReader



## CardReader Power switch

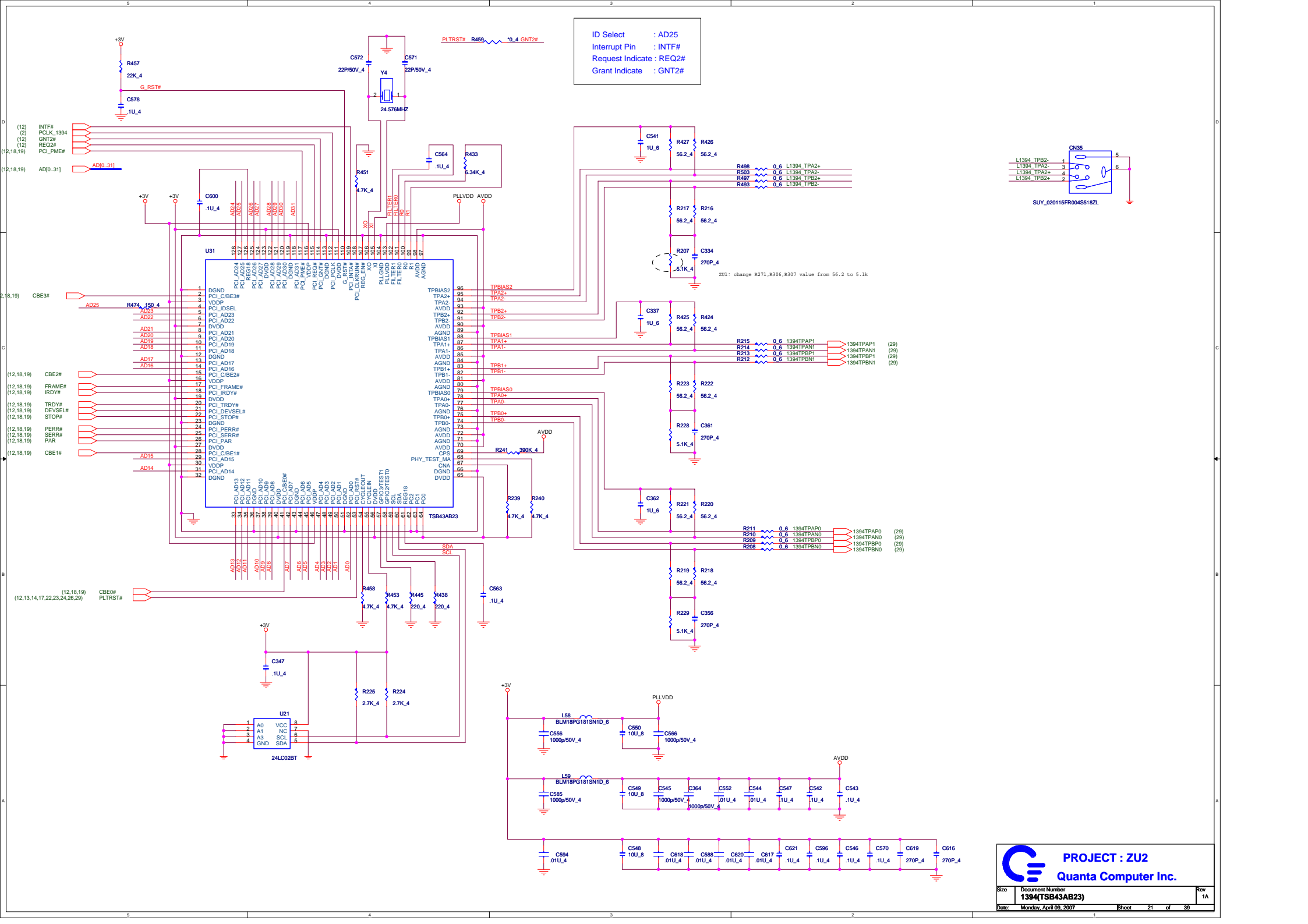


## PCMCIA

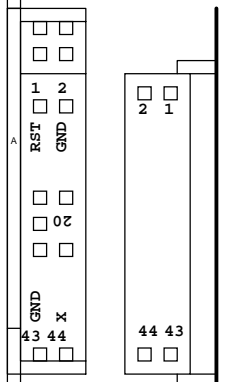
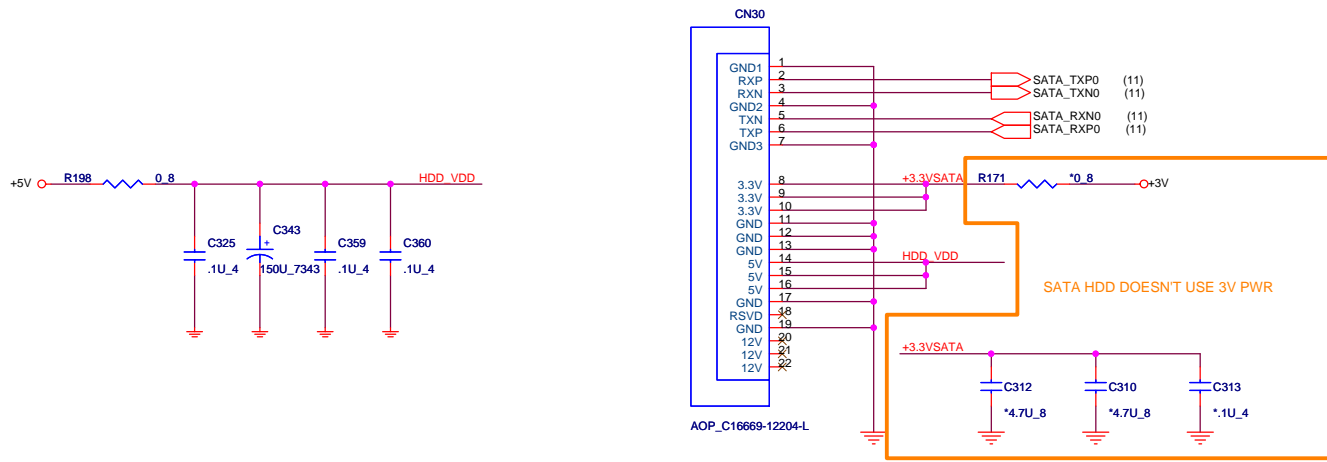


**PROJECT : ZU2**  
**Quanta Computer Inc.**

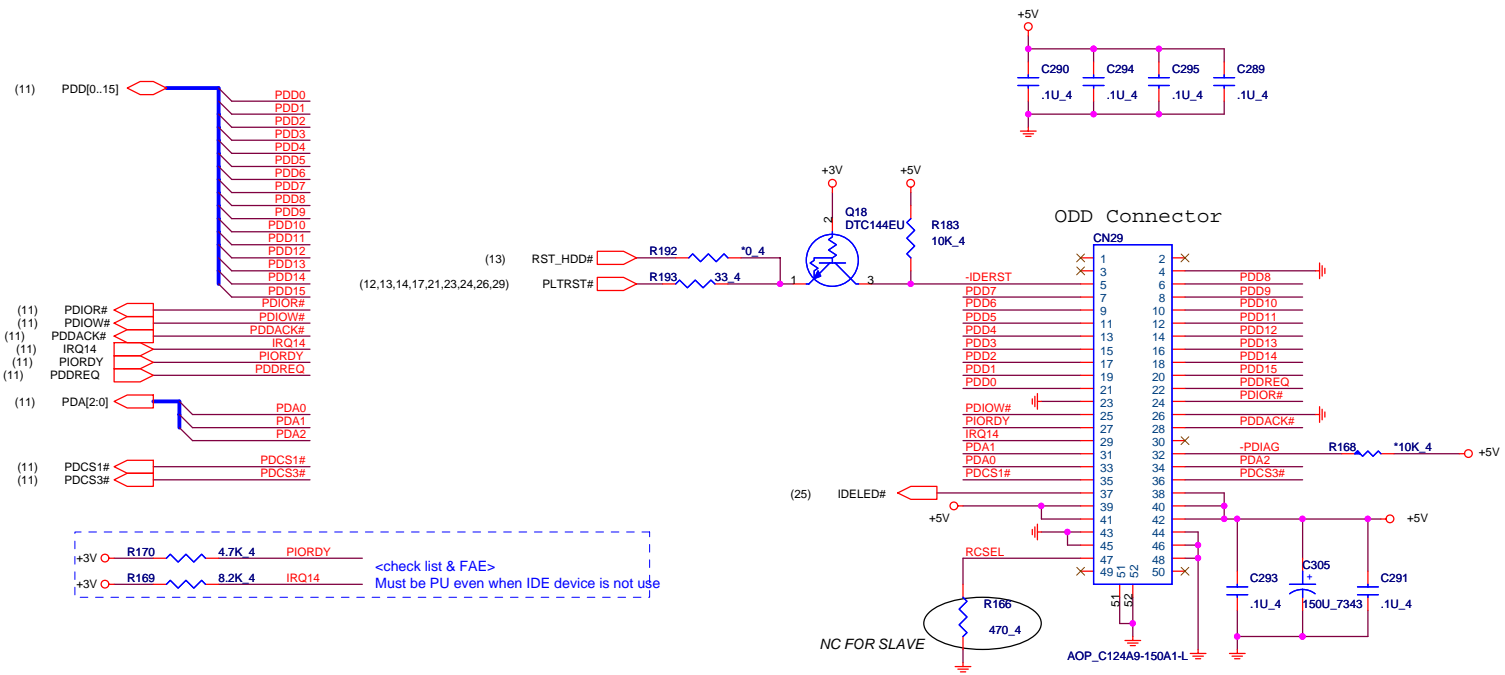
Size	Document Number	Rev
	<b>CARD Reader &amp; PCMCIA SLOT</b>	1A
Date:	Monday, April 09, 2007	Sheet 20 of 39



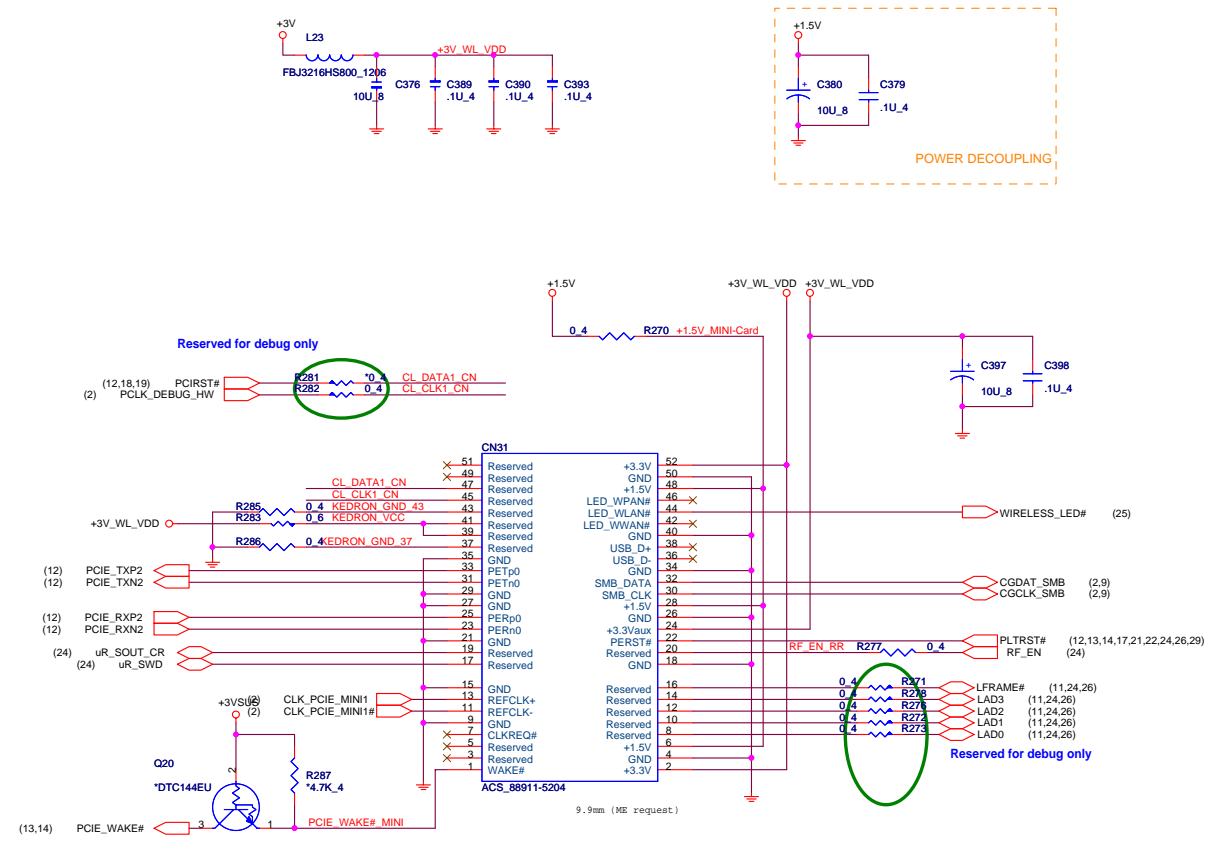
SATA HDD



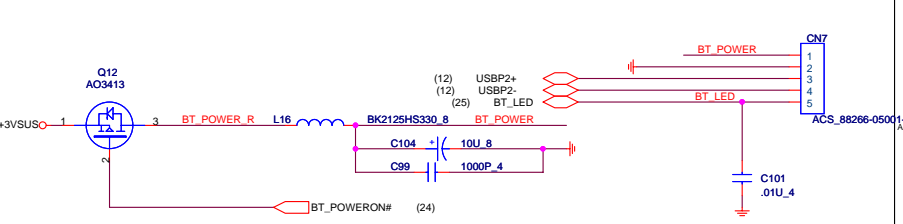
PATA ODD



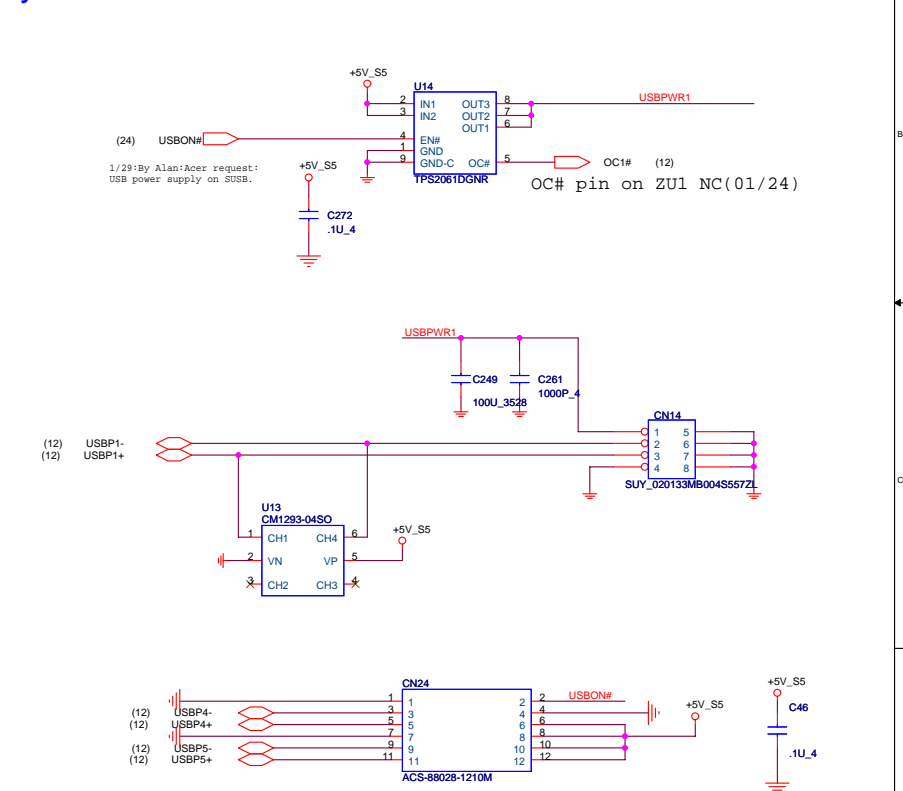
MINI-Card

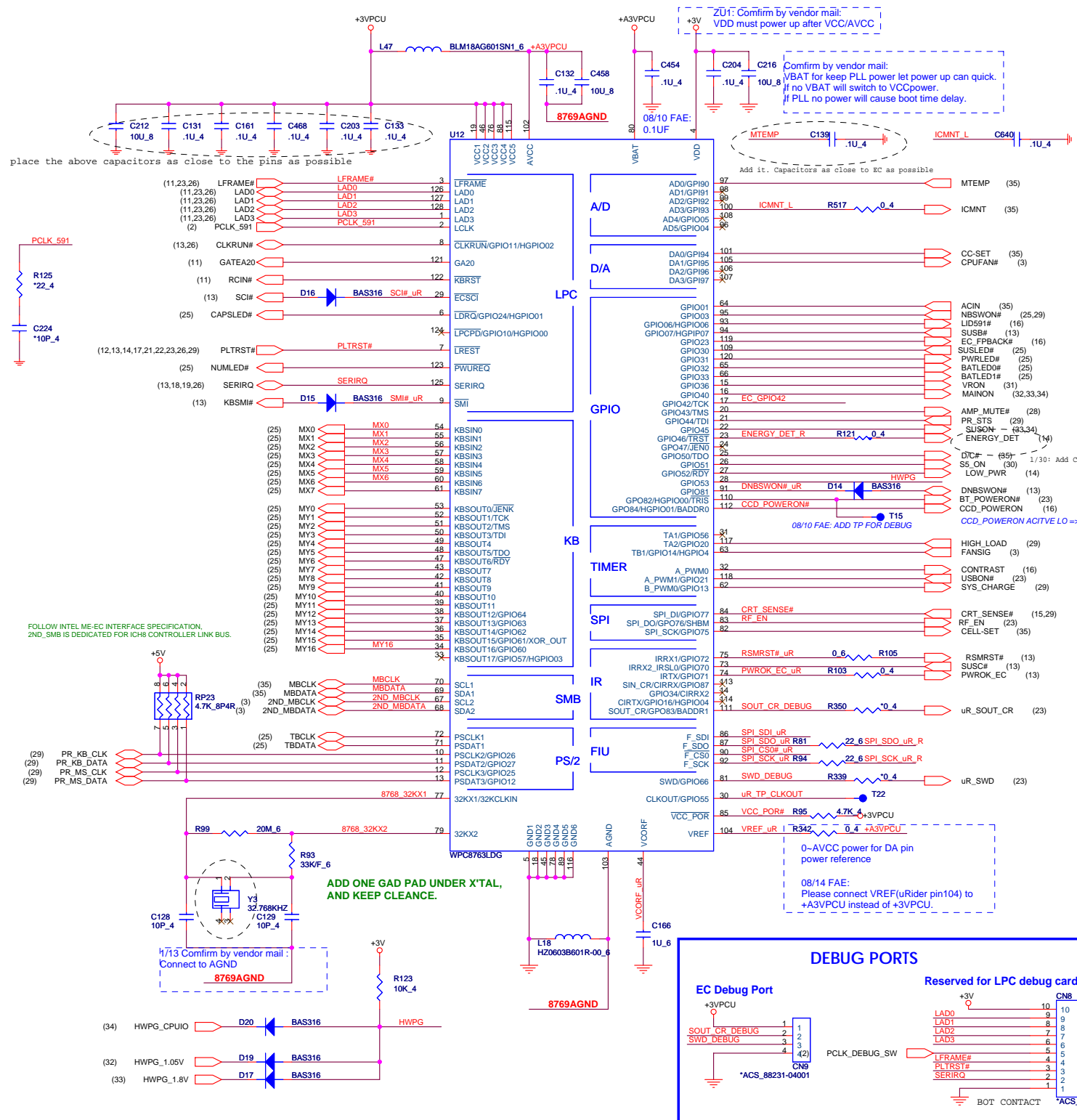


BLUETOOTH MODULE CONNECTOR



System USB





### SM BUS PU

MBCLK R96 4.7K 4  
MBDATA R97 4.7K 4  
2ND\_MBCLK R100 4.7K 4  
2ND\_MBDATA R98 4.7K 4

EC\_GPIO42 R122 4.7K 4  
CRT\_SENSE# R337 4.7K 4

1/29: confirmed Anda: take out R398

### I/O ADDRESS SETTING

I/O Address		
BADDR1-0	Index	Data
0 0	XOR TREE TEST MODE	
0 1	CORE DEFINED	
1 0	2Eh	2Fh
1 1	164Eh	164Fh

SHBM=0: Enable shared memory with host BIOS

BADDR0 CCD\_POWERON# R353 10K 4  
BADDR1 SOUT\_CR\_DEBUG R352 10K 4  
SHBM RF\_EN R338 10K 4

1/13 Confirm by vendor mail :  
Disabled ('1') if using FWH device on LPC.  
Enabled ('0') if using SPI flash for both system BIOS and EC firmware

### ACER ID

U6

2ND\_MBCLK 6  
2ND\_MBDATA 5

1 1  
2 2  
3 3

WIP VCC GND  
4 4

24LC08

3VPCU

C70 10U 4

### SPI FLASH

SPI\_SDI\_uR R90 22.6 SPI\_SDI\_uR\_R

3VPCU

R53 10K 4

SPI\_SDI\_uR\_R 2  
SPI\_SDO\_uR\_R 5  
SPI\_SCK\_uR\_R 6  
SPI\_CS0#\_uR 1

SO HOLD WP VSS  
8 7 3 4

U24 W25X80VSSIG

3VPCU

C431 10U 4

1/29: By Alan: Per Winbond's request.  
Place the ,R596,R597,R598 as close as possible to the source.

1/13 Confirm by vendor mail :  
If the Southbridge enables 'Long Wait Abort' by default, the flash device should be 50MHz (or faster)

### BUTTON ON KEYBOARD MATRIX

MX0 (25)  
MX1 (25)  
MX2 (25)  
MX3 (25)  
MX4 (25)  
MX5 (25)  
MY16 (25)

WIRELESS\_SW# (25)  
BLUETOOTH\_SW# (25)

### INTERNAL KEYBOARD STRIP SET

MY0 R110 10K 4

3VPCU

### DEBUG PORTS

#### EC Debug Port

3VPCU

SOUT\_CR\_DEBUG 1  
SWD\_DEBUG 2  
SWD\_DEBUG 3  
SWD\_DEBUG 4

CN9 \*ACS\_88231-04001

#### Reserved for LPC debug card

3V

LAD0 10  
LAD1 9  
LAD2 8  
LAD3 7  
LAD4 6  
LAD5 5  
LAD6 4  
LAD7 3  
LAD8 2  
LAD9 1

PCLK\_DEBUG\_SW

CN8 \*ACS\_88502-1001

BOT CONTACT

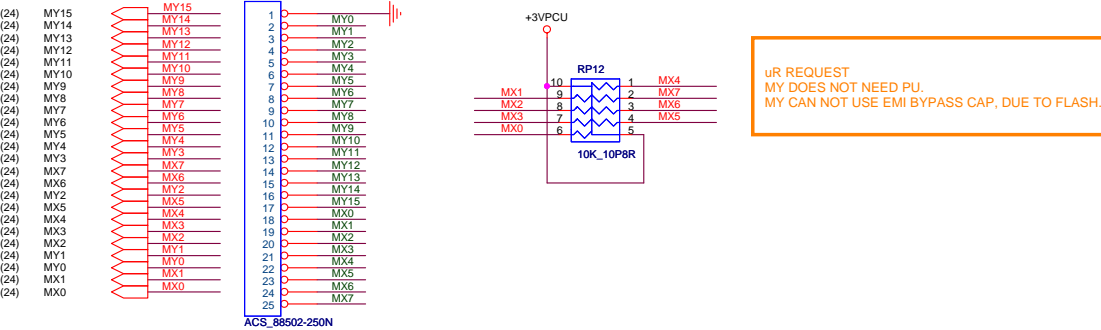
### PROJECT : ZU2

#### Quanta Computer Inc.

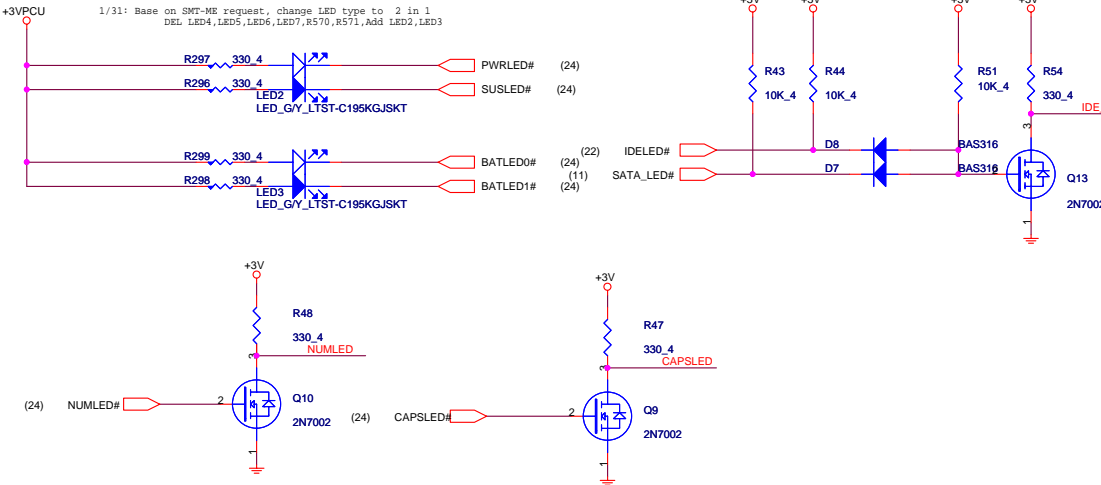
Size	Document Number	Rev
	EC (PC8763LDG) FLASH	1B

Date: Monday, April 09, 2007 Sheet 24 of 39

INT K/B



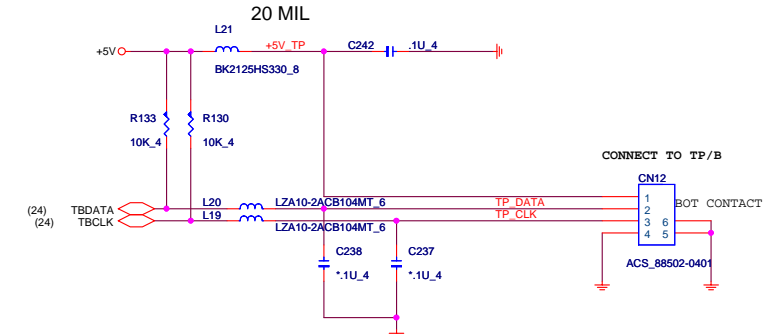
LED



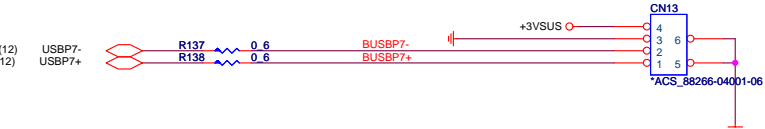
Function Board



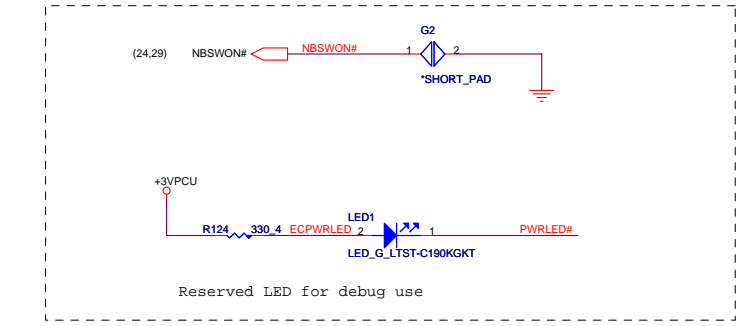
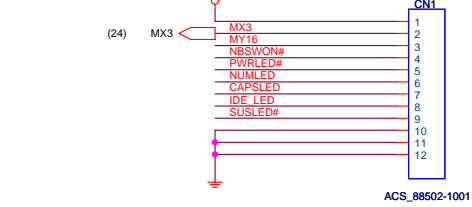
TOUCH PAD

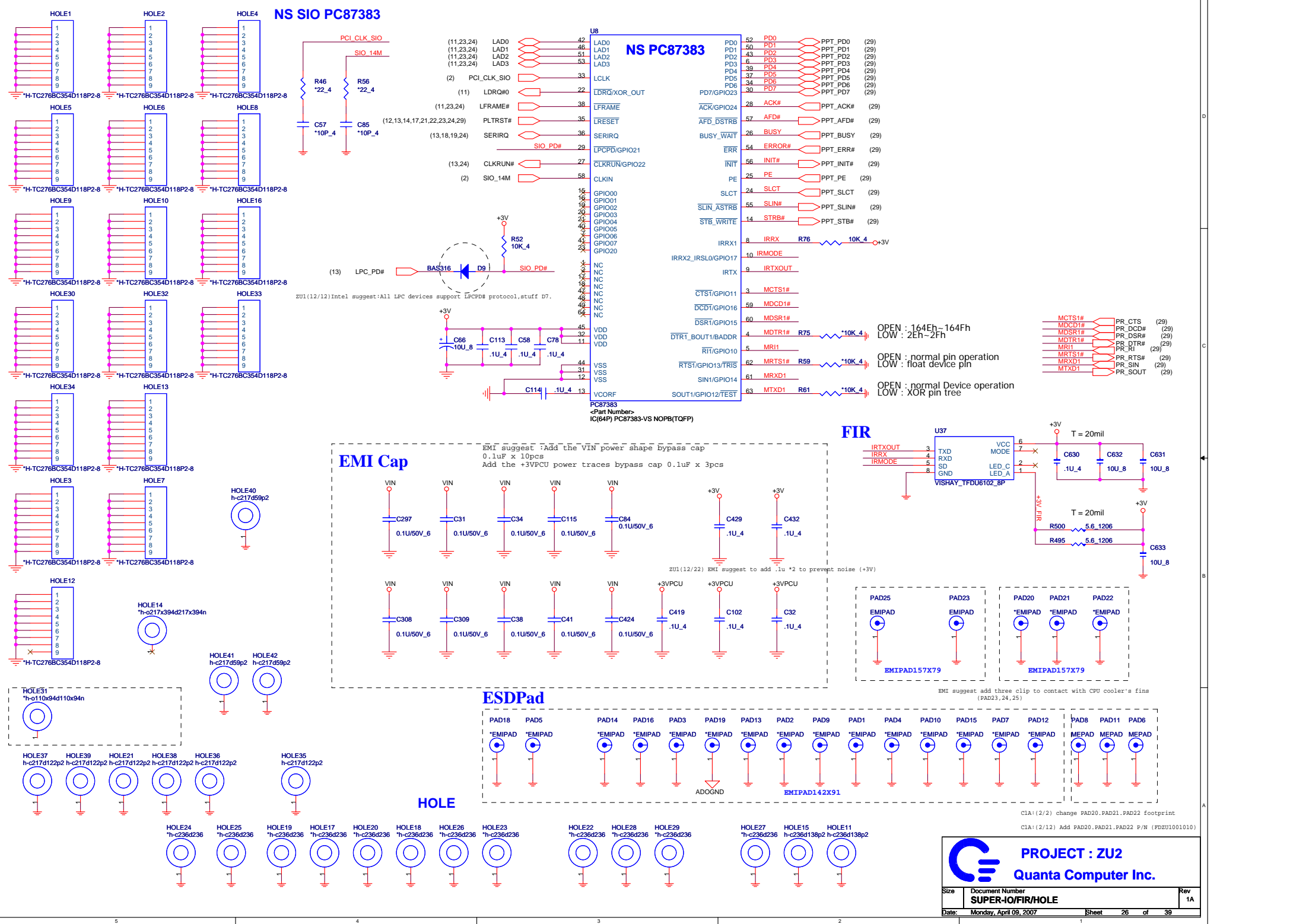


Finger Printer

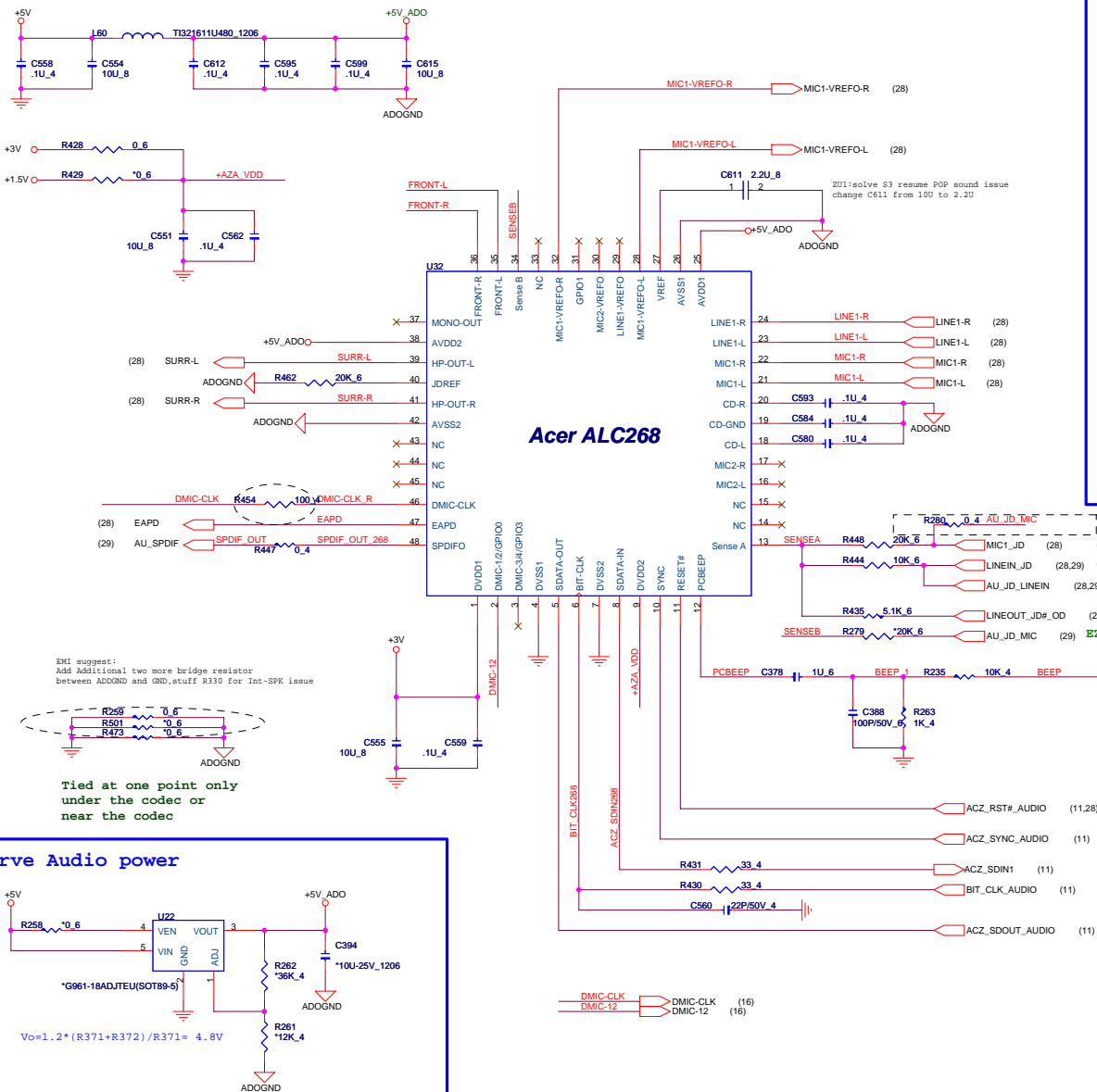


LED Board

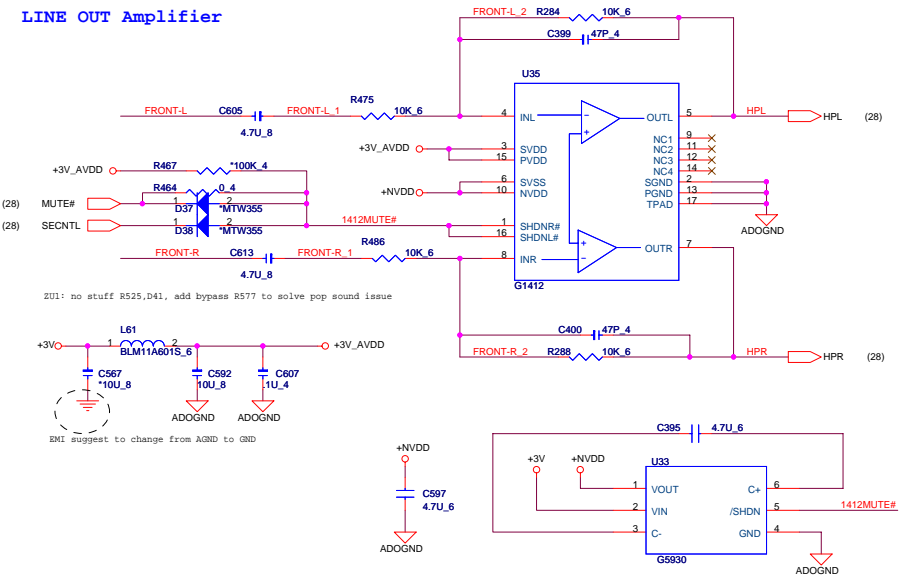




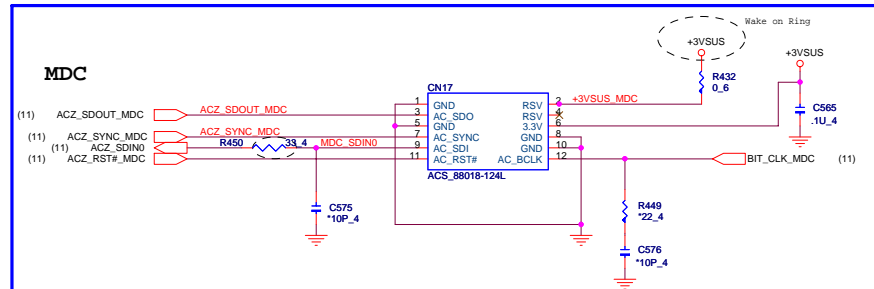
## CODEC (ALC268)



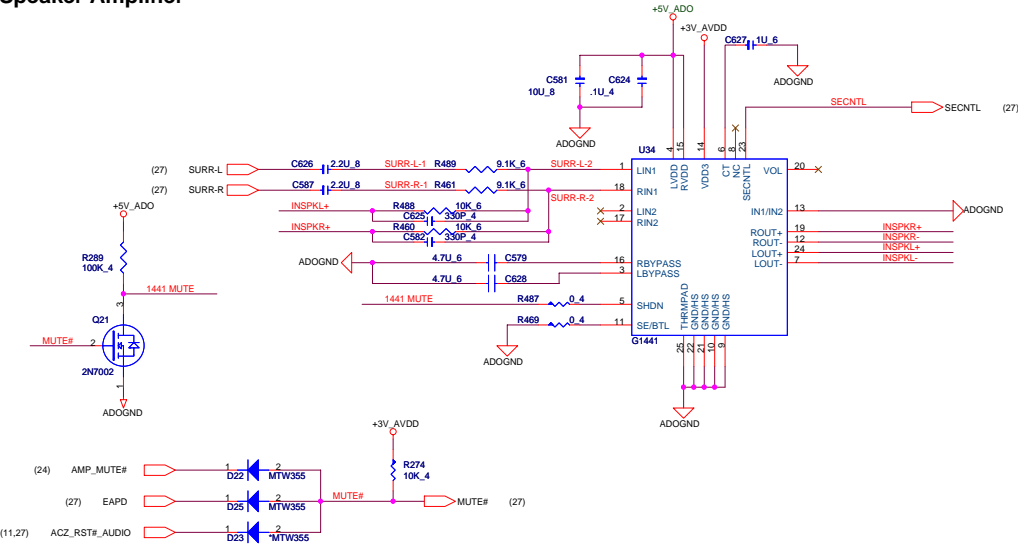
## LINE OUT Amplifier



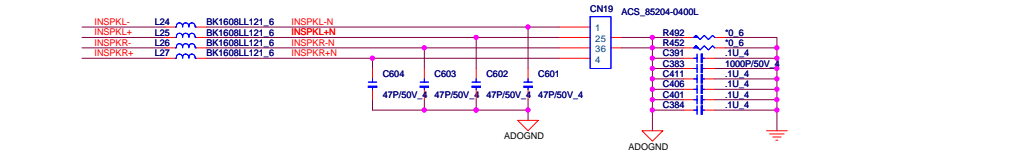
## MDC



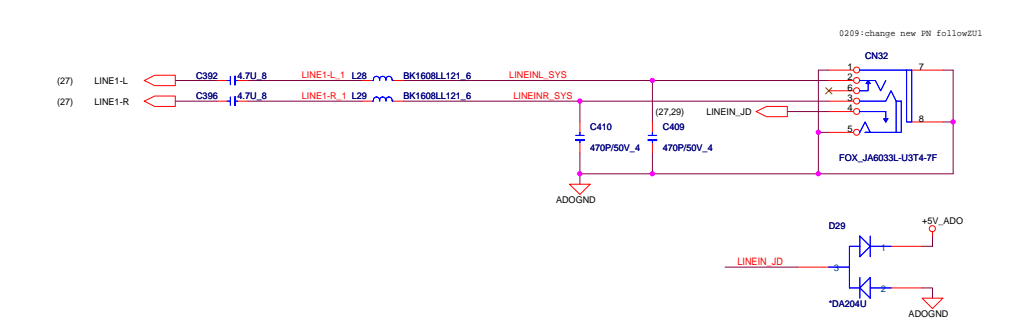
Speaker Amplifier



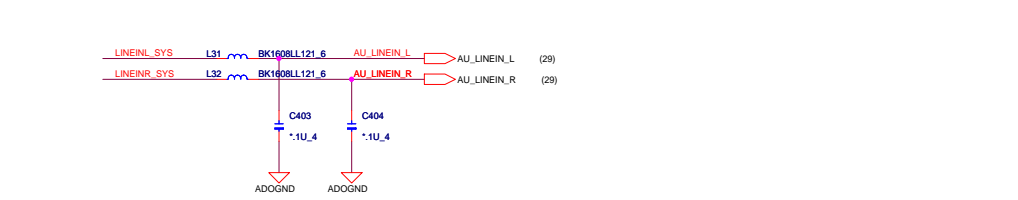
SPEAKER



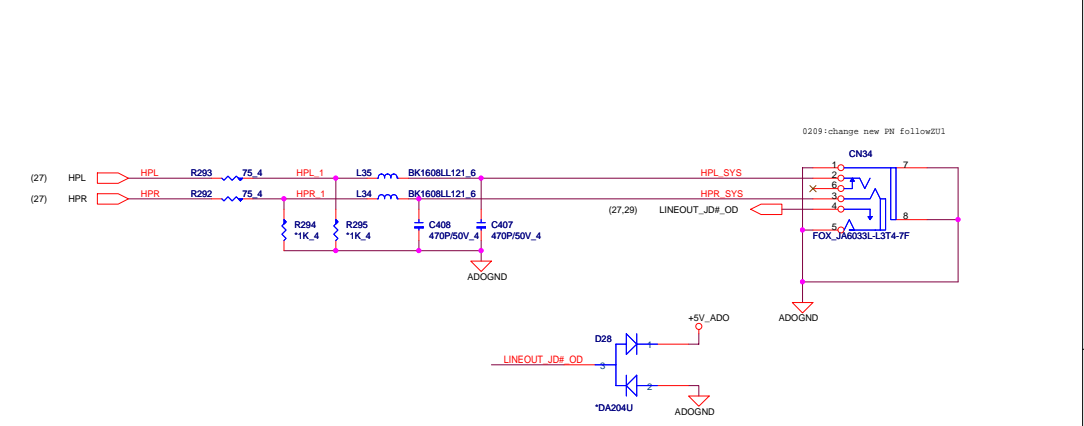
SYSTEM LINE IN



Docking LINE IN



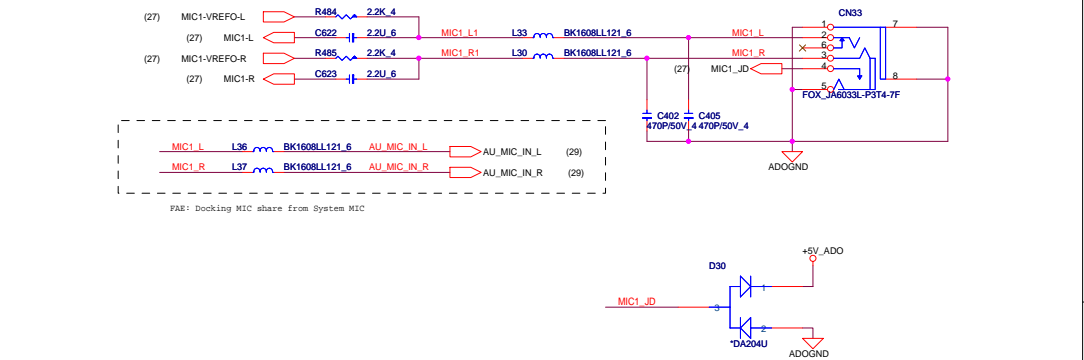
SYSTEM LINE OUT

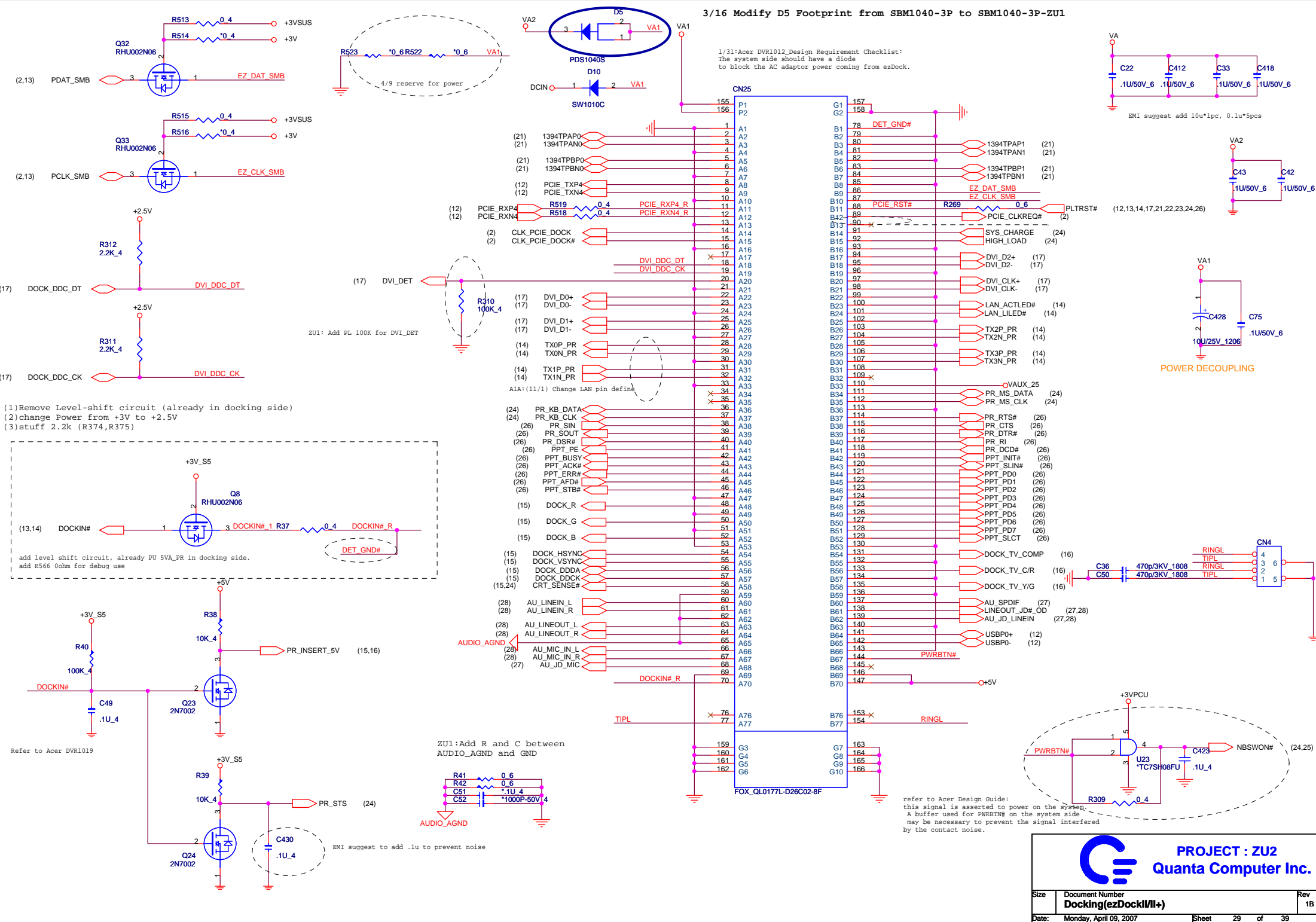


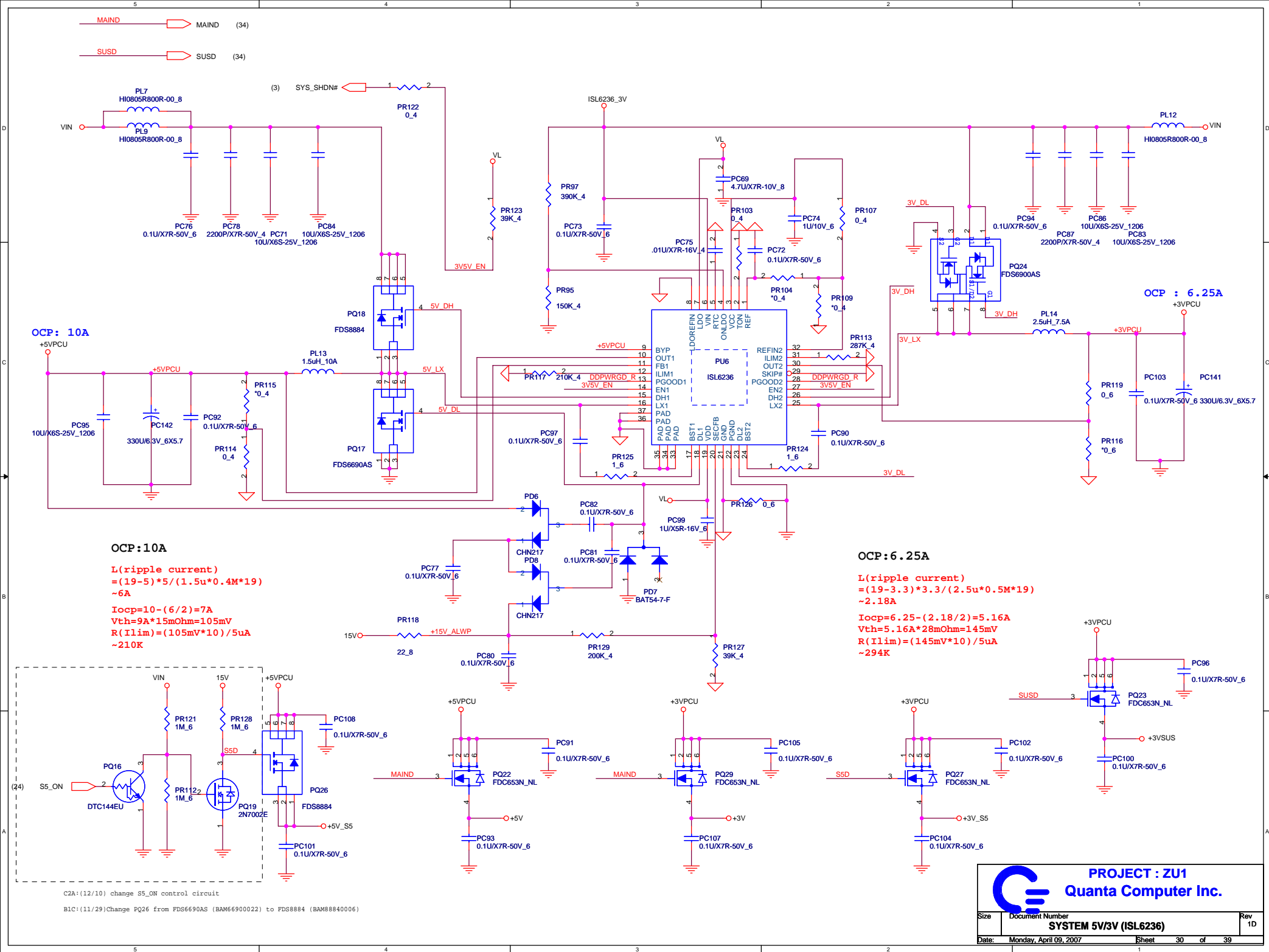
Docking LINE OUT/SPDIF

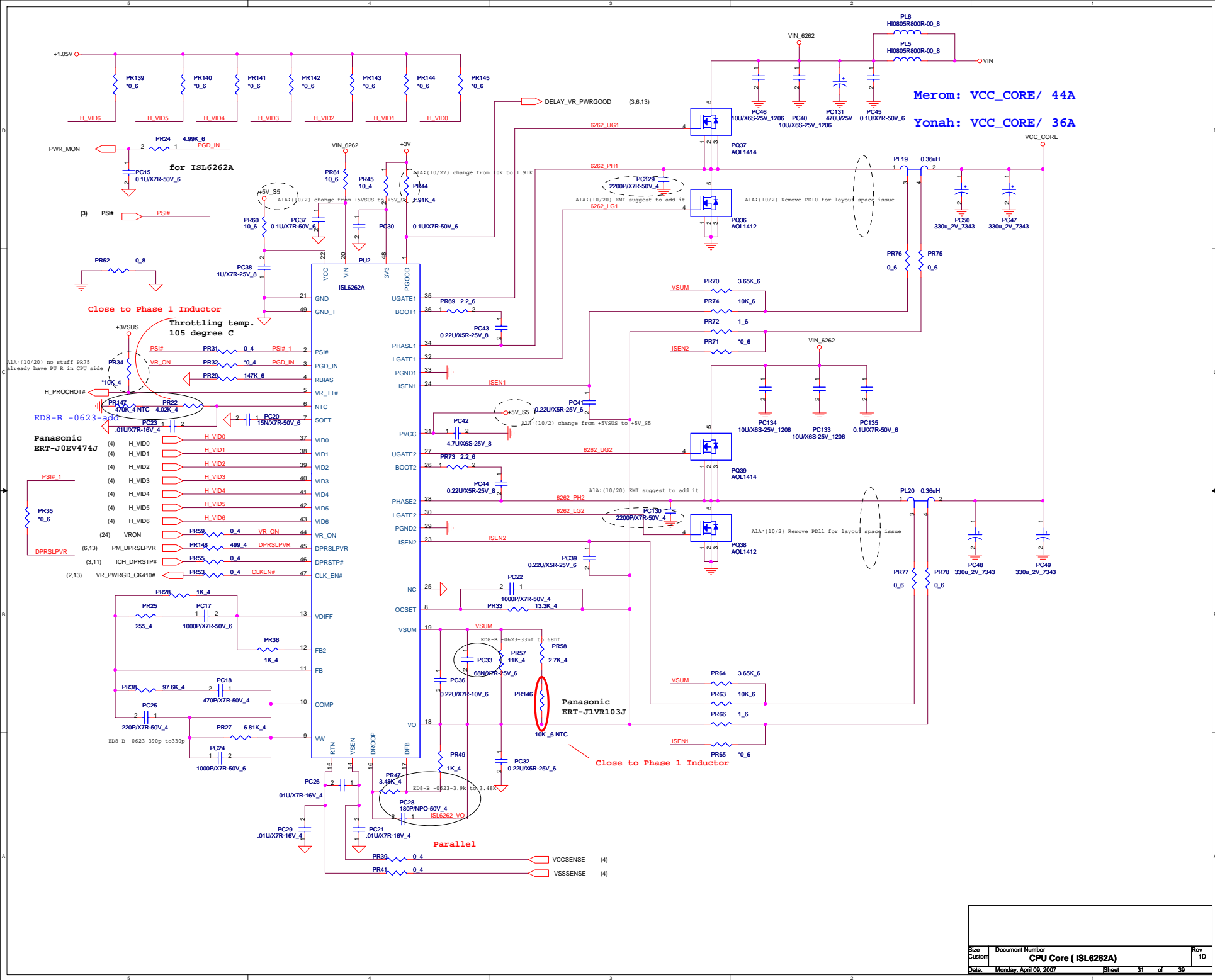


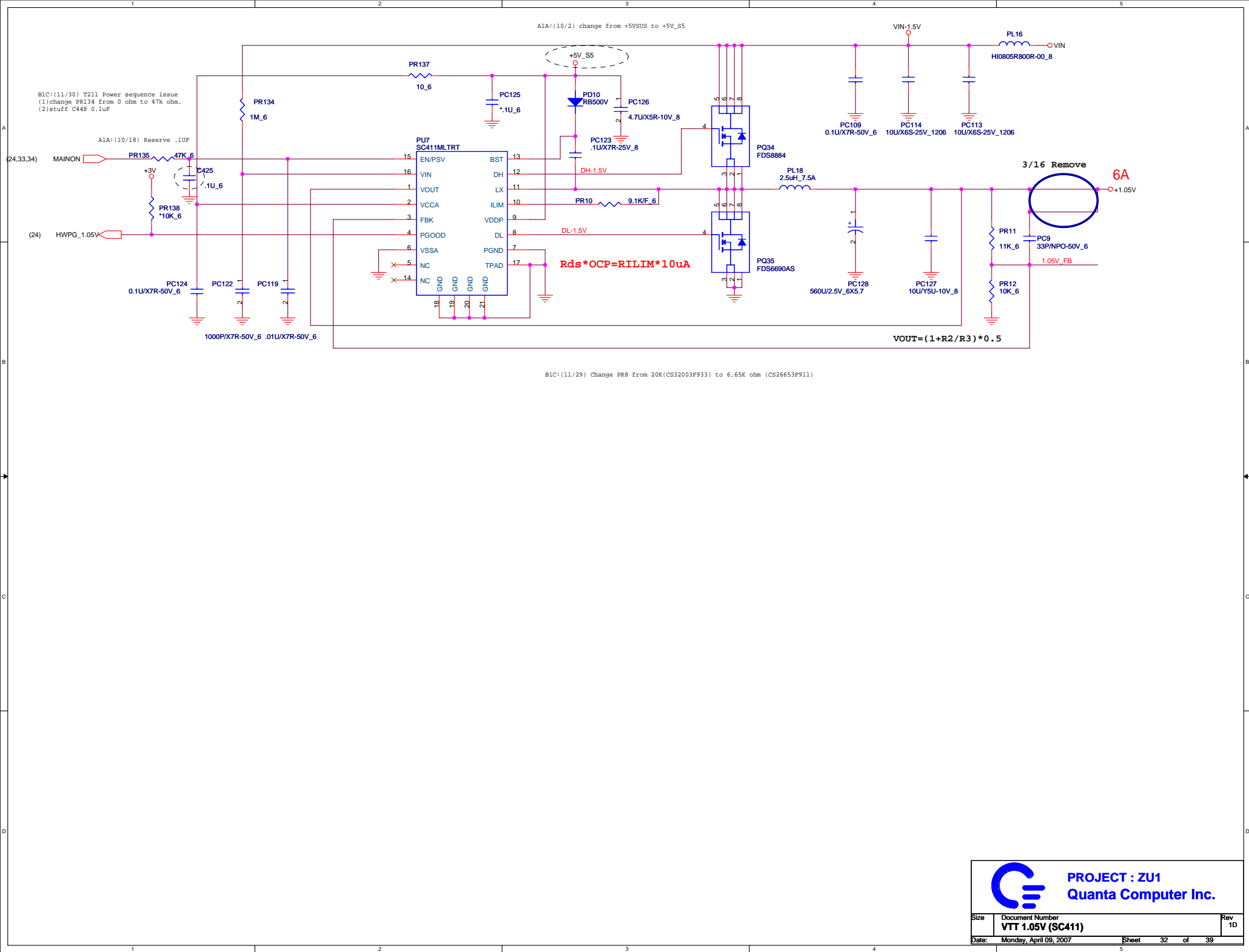
SYSTEM MIC





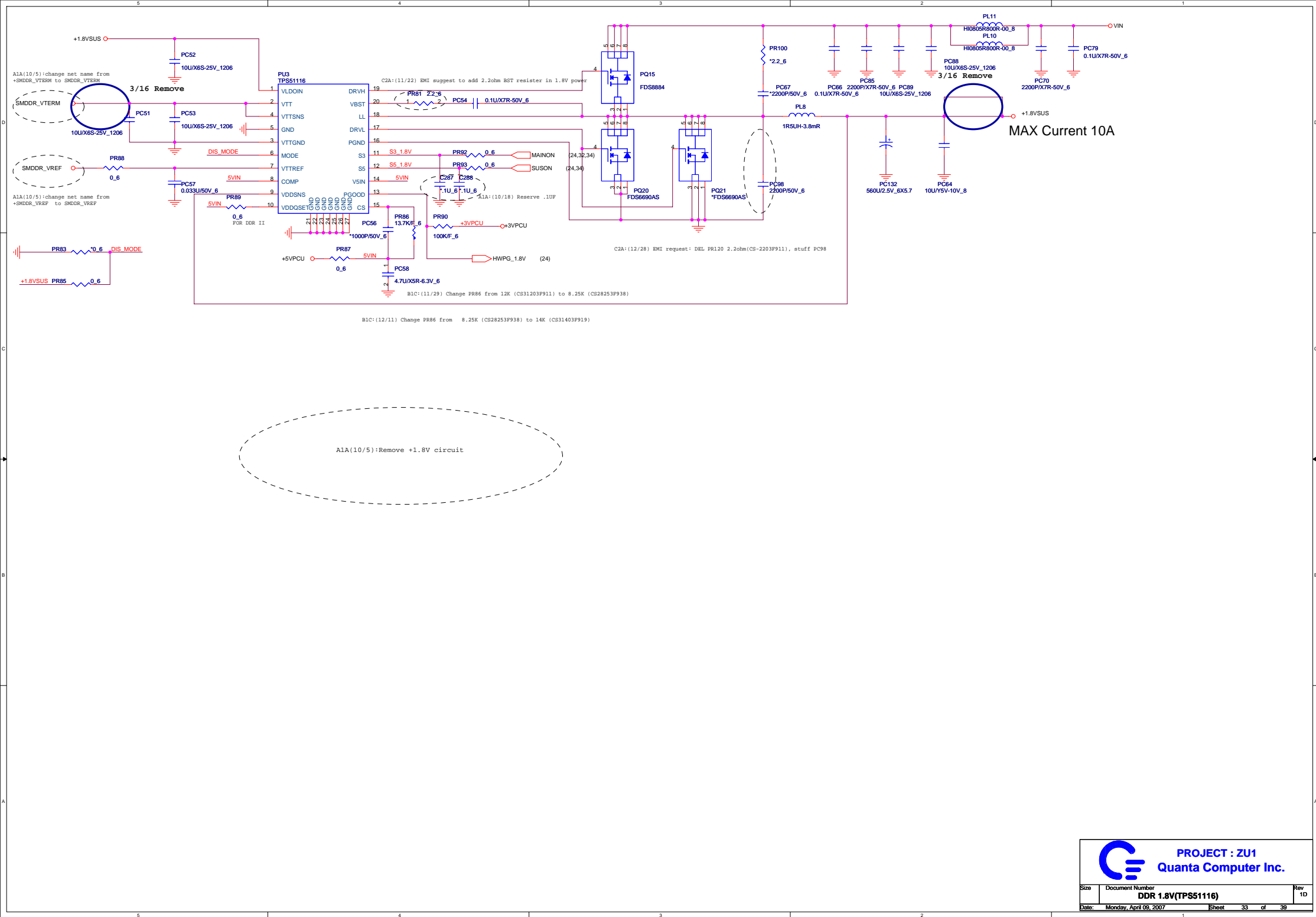






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Quanta Computer Inc.

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	VTT 1.05V (SC411)	1D
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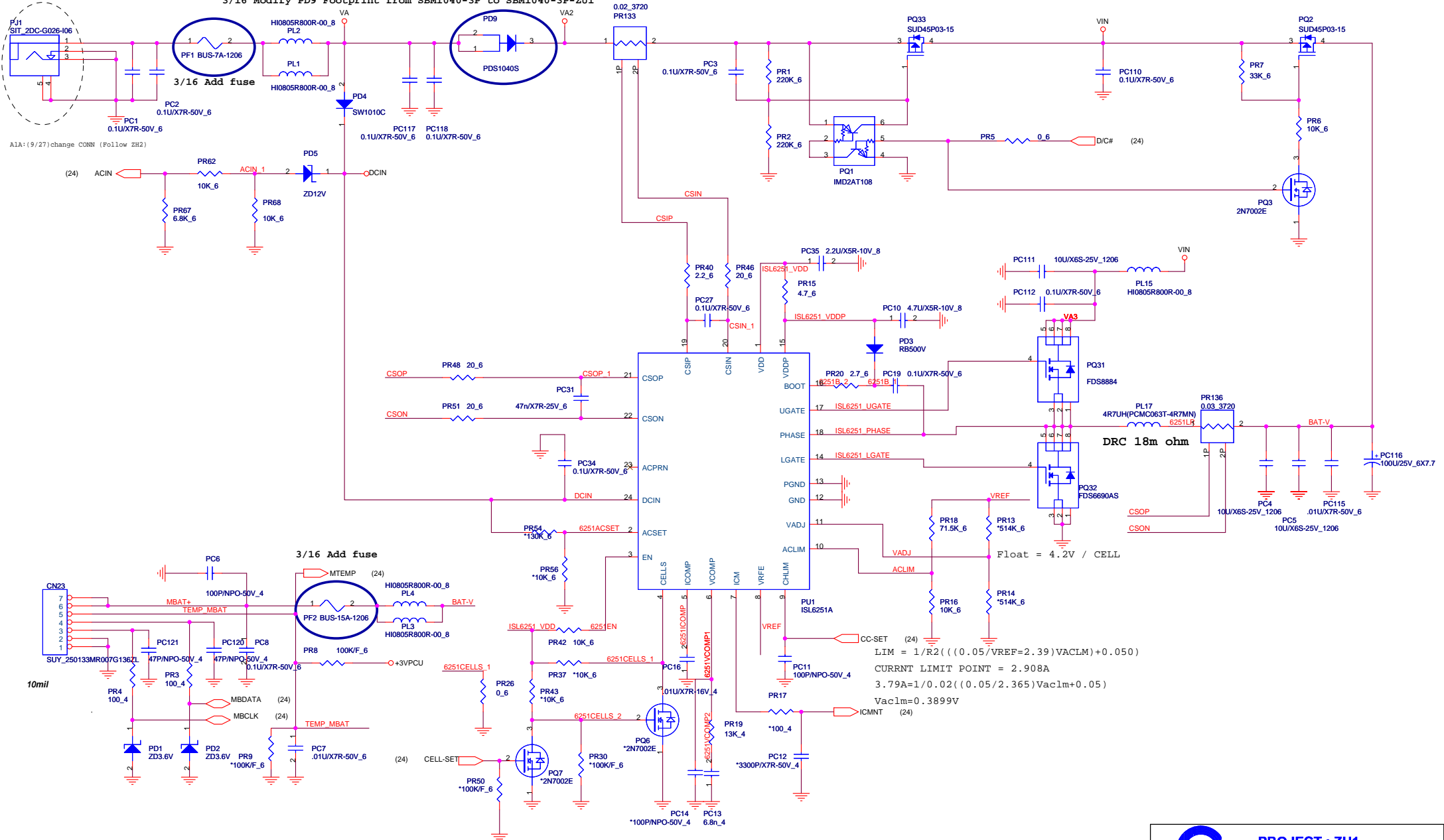


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Quanta Computer Inc.

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	DDR 1.8V(TPS51116)	1D
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# 3/16 Modify PD9 Footprint from SBM1040-3P to SBM1040-3P-ZU1



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Quanta Computer Inc.

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	Charger (ISL6251)	1D
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